

# ACER\_BAP10/BXP10 MAIN BOARD---A01

2010.04.02

<b>INVENTEC</b>			
TITLE			
ACER_BAP10/BXP10			
TIME			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01
SHEET		1	of 45

# 1. Schematic Page Description :

## Montavina Schematic Ver : X01

- |                             |                             |                           |
|-----------------------------|-----------------------------|---------------------------|
| 1. Title                    | 17. Processor(2/4)          | 33. HDD/BT/TPM/GP/FP/LID  |
| 2. Schematic Page DESCR     | 18. Processor(3/4)          | 34. CR_AU6437-GEF         |
| 3. Block Diagram            | 19. Processor(4/4)          | 35. KBC ITE8502E*         |
| 4. Power Block Diagram      | 20. PCH_RTC,SATA,PCI-E,CLK  | 36. EASY PORT             |
| 5. Annotations              | 21. PCH_DMI,MISC,LVDS,CRT   | 37. Hybrid Switch (1/2)   |
| 6. Schematic Modify         | 22. PCH_USB, PCI,NVRAM,XDP  | 38. POWER SEQUENCE        |
| 7. Timing Diagram           | 23. PCH Power 1             | 39. SW/LED                |
| 8. PWR_Adaptor in/Charge    | 24. PCH Power 2             | 40. FP board              |
| 9. PWR_CPU Core Power       | 25. PCH Power 3 (GND)       | 41. GP Button board       |
| 10. PWR_Graphics Core       | 26. Clock Generator         | 42. SW Button/BAP10 board |
| 11. PWR_DDR PWR             | 27. DDR3 SDRAM SO-DIMM 0/1  | 43. SW Button/BXP10 board |
| 12. PWR_1.1VS_VTT/1.1VS     | 28. LCD,CMA,CRT PLUG        | 44. LED Board/BXP10 board |
| 13. PWR_5VA/5VLA/3VA/3VLA   | 29. USB/MDC                 | 41. TPM board             |
| 14. PWR_3VS/5VS/1.8VS/5VUSB | 30. LAN(BCM57760)           |                           |
| 15. PWR_Reserve             | 31. CX20672-11Z / MIC / SPK |                           |
| 16. Processor(1/4)          | 32. UMTS/WLAN               |                           |

## 2. PCI & IRQ & DMA Description :

IDSEL	CHIP	PCIINT	CHIP	Interface	REQ	CHIP
None		None			None	

## 3. USB & PCI-Express & SATA Description :

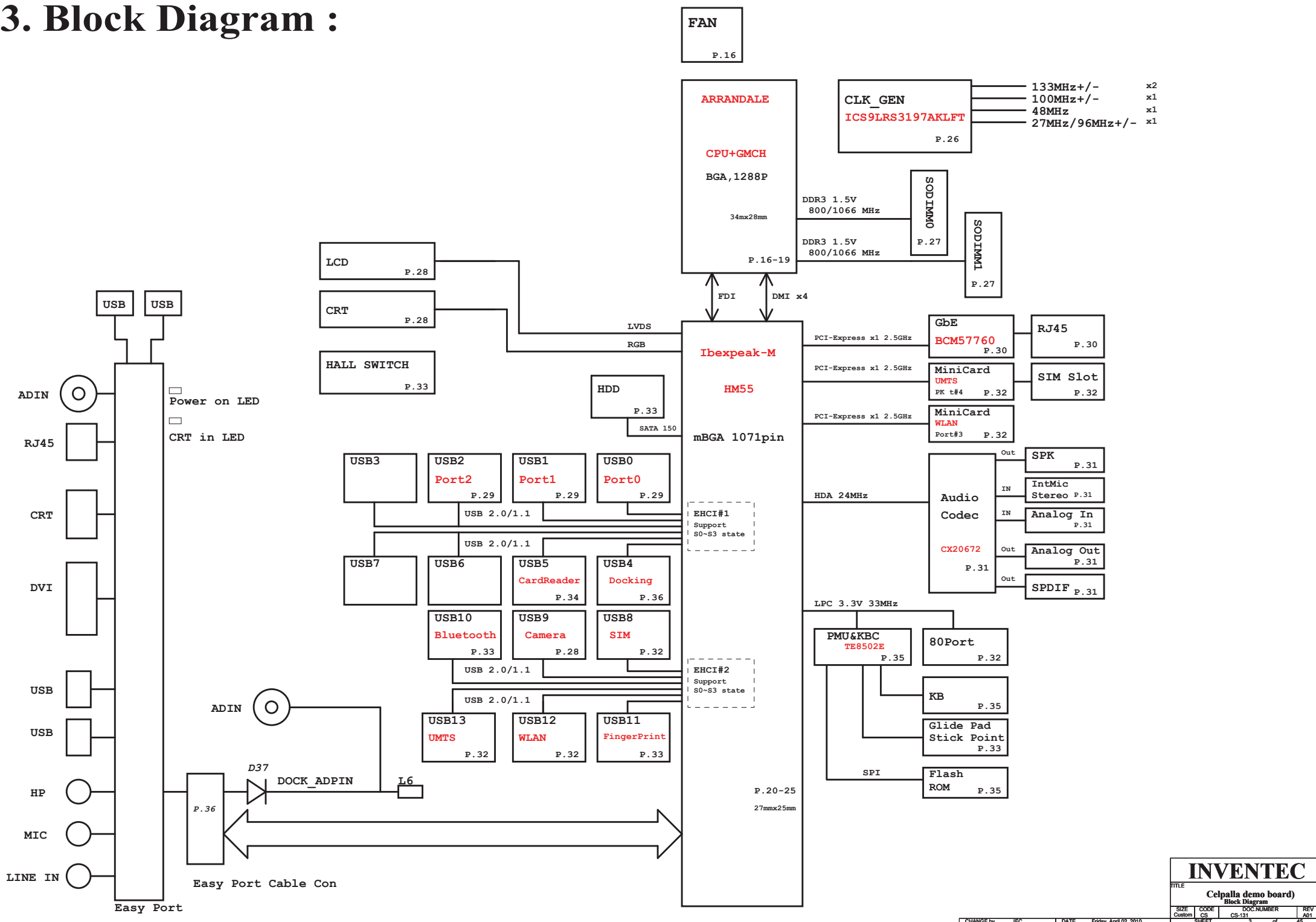
USB Port	DEVICE	USB Port	DEVICE	PCI-E	DEVICE	SATA	DEVICE
Port 0	System (ESATA)	Port 7	Bluetooth	Port 1	New Card	Port 1	HDD
Port 1	System	Port 8		Port 2	Docking	Port 2	E-SATA
Port 2	System	Port 9	Web Cam	Port 3	Mini Card(WLAN)	Port 4	BAY
Port 3	System	Port 10		Port 4	Mini Card(3G)	Port 5	None
Port 4	CardReader	Port 11	FingerPrint	Port 5	Mini Card(ROBSON)		
Port 5		Port 12		Port 6	Giga-LAN		
Port 6		Port 13	3G				

**INVENTEC**

TITLE: ACER BAP10/BXP10  
schematics page DESCR

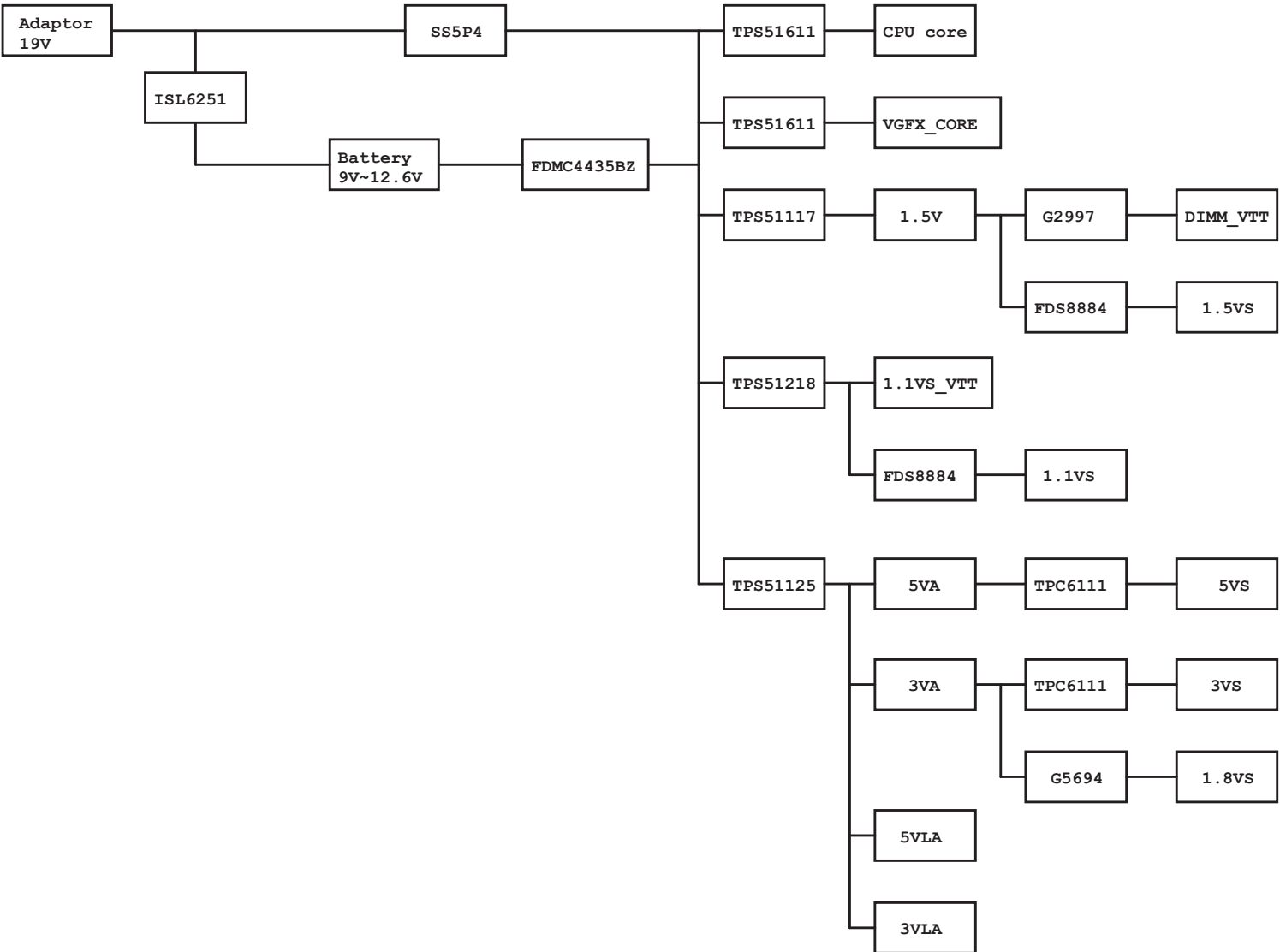
SIZE: Custom	CODE: CS	DOC NUMBER: CS-131	REV: A01
SHEET: 2		of 45	

# 3. Block Diagram :



<b>INVENTEC</b>			
TITLE <b>Celpalla demo board)</b>			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01
SHEET		3	of 45

# Power Block Diagram :



# 4. Net name Description :

## Voltage Rails

DCIN	Primary DC system power supply
3VLA	3.3V always on power rail by DCIN
5VLA	5.0V always on power rail by DCIN
EC_3VLA	3.3V always on power rail by 5VAUXON
3VA	3.3V always on power rail by LATCH_ON
5VA	5.0V always on power rail by LATCH_ON

3VM	3.3V power rail by SUSM#
1.05VM	1.05V switched power rail by SUSM#

1.5V	1.5V switched power rail by SUSC#
1.8V	1.8V power rail by SUSC#

3VS	3.3V power rail by SUSB#
5VS	5.0V power rail by SUSB#
1.5VS	1.5V power rail by SUSB#
1.05VS	1.05V power rail by SUSB#
PWR_DIMM_VTT	0.75V DDR Termination Voltage by SUSB#

VGFX_CORE	1.05V power rail for UMA by SUSB#
PEG_1.8VS	1.8V switched power rail for NB9x by SUSB#
PEG_PEX_1.1VS	1.1V switched power rail for NB9x by SUSB#
PEG_NVDD	Variable switched power rail for NB9x by SUSB#

Vcore_CPU	Core switched power rail for CPU
-----------	----------------------------------

## Part Naming Conventions





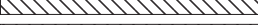


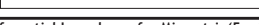
C = Capacitor	Q = Transistor
CN = Connector	R = Resistor
D = Diode	RP = Resistor Pack
F = Fuse	U = Arbitrary Logic Device
L = Inductor	Y = Crystal and Osc

## Name Suffix

# = Active Low signal
NU = No Stuff

# 5. Board Stack up Description

## PCB Layers

Layer 1		Component Side, Microstrip signal Layer
Layer 2		Ground Plane
Layer 3		Stripline Layer
Layer 4		Power Plane
Layer 5		Stripline Layer
Layer 6		Stripline Layer
Layer 7		Ground Plane
Layer 8		Solder Side, Microstrip signal Layer

	Differential Impedance for Microstrip(5-mils)	Differential Impedance for Stripline(4-mils)
Host Clock	95 ohm +/- 20%	100 ohm +/- 20%
PCI-E Clock	95 ohm +/- 20%	100 ohm +/- 20%
DDR2 CLK	70 ohm +/- 20%	70 ohm +/- 20%
DDR2 Strobe	85 ohm +/- 20%	90 ohm +/- 20%
DMI Bus	95 ohm +/- 20%	100 ohm +/- 20%
PCIE Bus	95 ohm +/- 20%	100 ohm +/- 20%
SDVO	95 ohm +/- 20%	100 ohm +/- 20%
SATA	95 ohm +/- 20%	100 ohm +/- 20%
USB	90 ohm +/- 20%	95 ohm +/- 20%
LVDS		100 ohm +/- 20%
Lan	95 ohm +/- 20%	100 ohm +/- 20%

Power Rail	Destination	Voltage	S0 Current
VCC_CORE	Penryn HFM: LFM:	1.3319V-1.4375V-1.4591V 0.9221V-0.9625V-0.9739V	36A
1.05VS	Penryn: AGTL+ termination Cantiga GM: Core Cantiga GM: PCIE Cantiga GM:Core+IMEL+HSIO Cantiga GM:VCC_GMCH Cantiga GM:VCCA_SM_CK and NCTF Cantiga GM:VCC_DMI Cantiga GM:VCCA_SM Cantiga GM:VTT ICH9M:VCC1_05 ICH9M:DMI ICH9M:CPU_IO	1V-1.05V-1.10V 0.997V-1.05V-1.102V 0.9975V-1.05V-1.1025V 0.9975V-1.05V-1.1025V 0.997V-1.05V-1.102V 0.997V-1.05V-1.102V 0.997V-1.05V-1.102V 0.997V-1.05V-1.102V 0.997V-1.05V-1.102V 0.997V-1.05V-1.102V 0.997V-1.05V-1.102V	4.5A 8.7A 1.78A 2.898A 10.154A 37.95mA 456mA 747.5mA 852mA 1.634A 48mA 2mA
1.5VS	Penryn PLL Cantiga GM: QDAC Cantiga GM: LVDS Cantiga GM: TVDAC Cantiga GM: Various PLLS analog supply Cantiga GM: VCC_SM_CK Cantiga GM: VCC_SM ICH9M:PCIE_ICH ICH9M:SATA_ICH ICH9M:VCC_GLAN Mini Card: Express Card:	1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.71V-1.8V-1.89V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V 1.425V-1.5V-1.575V	130mA 0.5mA 60.31mA 35mA 485mA 149.5mA 3.1625A 646mA 1.342A 80mA 650mA
1.5V	Cantiga GM: DDRIII System Memory	1.425V-1.5V-1.575V	3.1A(800M) 4.1A(1067M)
0.75VDDT	DDRIII:DDRIII Terminator:	0.7125V-0.75V-0.7875V	1.0A
3VS	Cantiga GM: HV CMOS Cantiga GM: VCCS_TVDDAC ICH9M:VCC3_3 ICH9M:VCCGLAN3_3 Thermal Sensor: Mini Card: UMTS Express Card: CLK Generator: ICS9LPRS397BKLF Mini Card: WirelessLan Bluetooth: Super I/O: IT8305E Azalia Codec: ALC262 Azalia MDC:	3.135V-3.3V-3.465V 3.135V-3.3V-3.465V 3.135V-3.3V-3.465V 3.135V-3.3V-3.465V 3.0V-3.3V-3.6V 3.135V-3.3V-3.465V 3.135V-3.3V-3.465V 3.0V-3.3V-3.6V 3.0V-3.3V-3.6V	105.3mA 78mA 308mA 1mA 5mA 1.3A 500mA
1.8VS	DVI	3.0V-3.3V-3.6V	120mA
3VA	ICH9M: RTC ICH9M:VCCSUS3_3 ICH9M:VCCCL3_3 ICH9M:VCCLAN3_3 LCD: Lan:82567LM Azalia MDC: Flash ROM: BIOS	2V-3.3V-3.465V 3.135V-3.3V-3.465V 3.135V-3.3V-3.465V 3.135V-3.3V-3.465V 3.0V-3.3V-3.6V 1.0V and 1.8V 3.0V-3.3V-3.6V	6uA 212mA 73mA 78mA 2A Each 1A
5VS	Cardreader: GL827 Azalia Codec: ALC262 HDD: SATA ODD: SATA Audio AMP: G1432 Inverter: WebCam	3.0V-3.3V-3.6V 3.0V-3.3V-3.6V 4.75V-5.0V-5.25V 4.75V-5.0V-5.25V 4.75V-5.0V-5.25V	Max: 1.5A ; R/W: 460mA ; STDBY: 70mA Max: 1.5A ; R/W: 900mA ; STDBY: 45mA
5VA	USB: x 2 ports USB and eSATA	5VA 5VA	1A 1.5A 2A
5VLA	Control Power		
3VLA	EC: ITE8512E	3.0V-3.3V-3.6V	300mA

**INVENTEC**

TITLE			
ACER BAP10/BXP10			
Authorizations			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01
SHEET			5 of 45

# 6.Schematic modify Item and History :

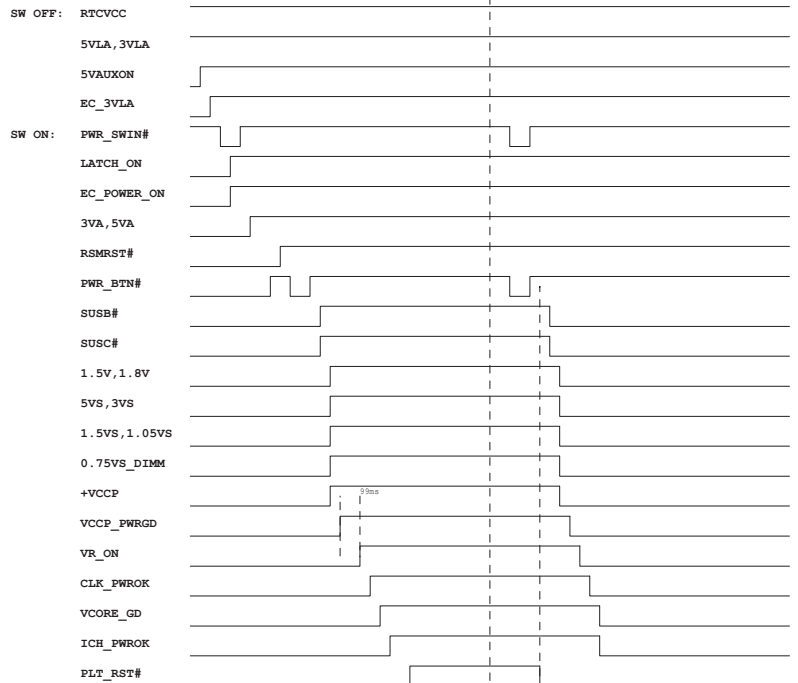
---

<b>INVENTEC</b>			
TITLE			
ACER_BAP10/BXP10			
schematics modify			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	AX1
SHEET		8	of 45

# 8.SYSTEM POWER SEQUENCE :

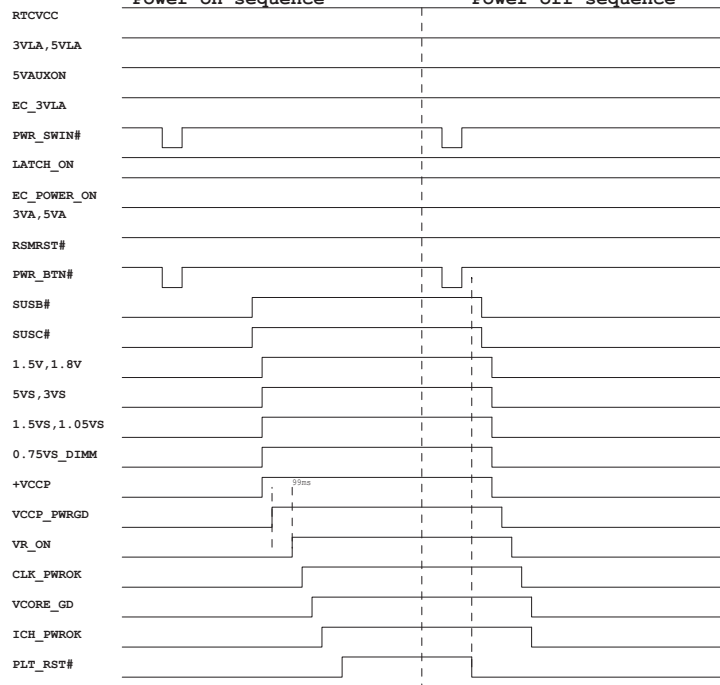
## Power on/off sequence AC insert(First)

Power on sequence      Power off sequence



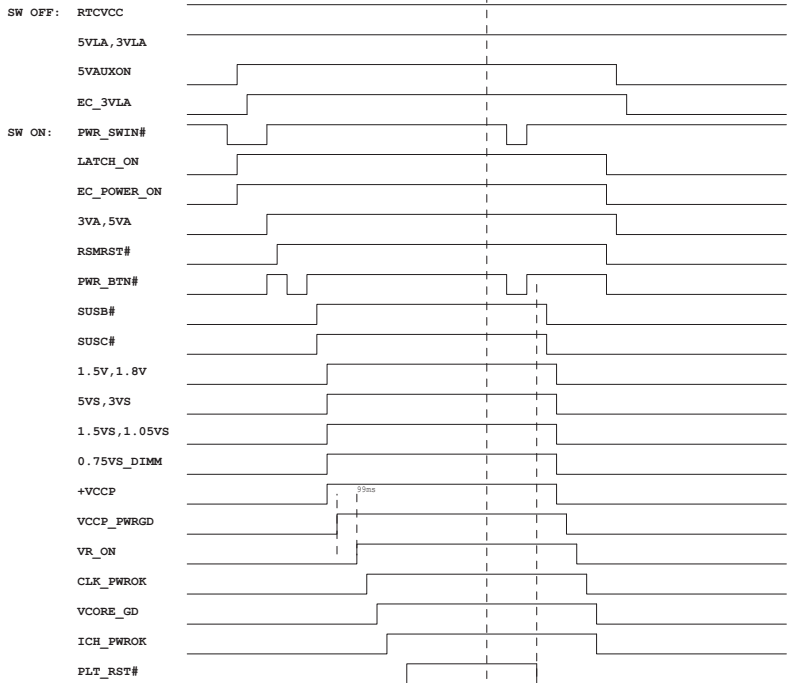
## Power on/off sequence AC insert (S4)

Power on sequence      Power off sequence



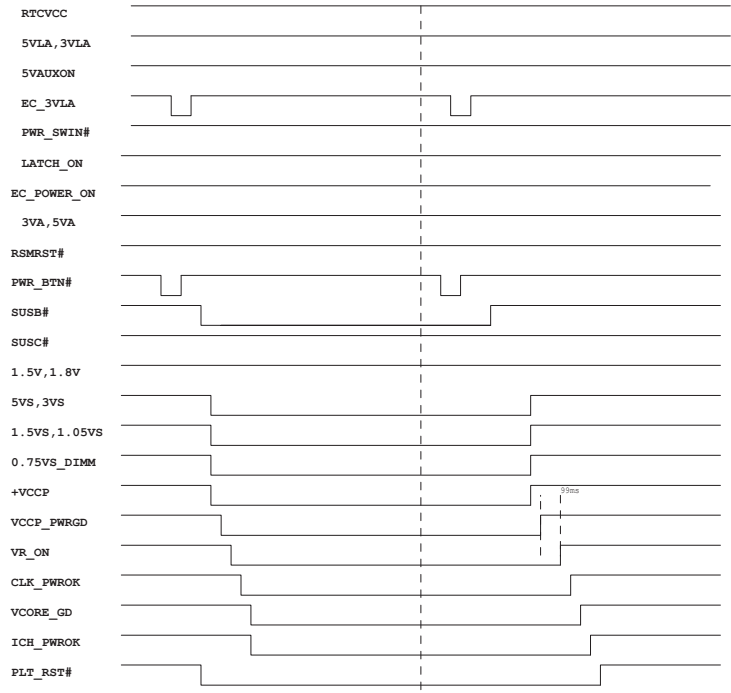
## Battery only Power on/off sequence

Power on sequence      Power off sequence

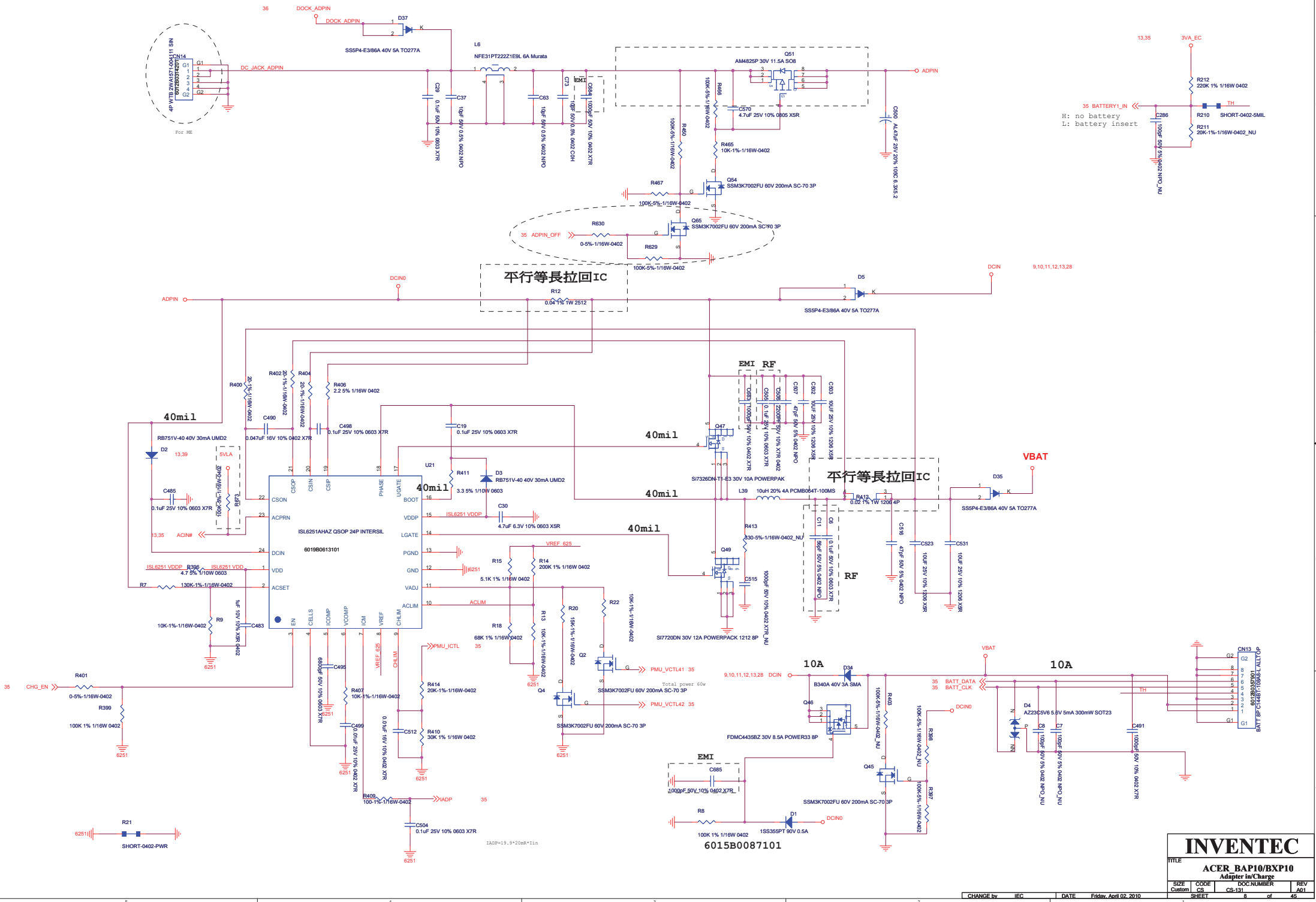


## Suspend resume sequence (S3)

Suspend sequence      Resume sequence



<b>INVENTEC</b>				
TITLE <b>ACER BAP10/BXP10</b>				
Timing Diagram				
SIZE	CODE	DOC NUMBER	REV	
Custom	CS	CS-131	A01	
CHANGE by IEC			DATE	Friday, April 02, 2010
SHEET			7	of 45



平行等長拉回IC

平行等長拉回IC

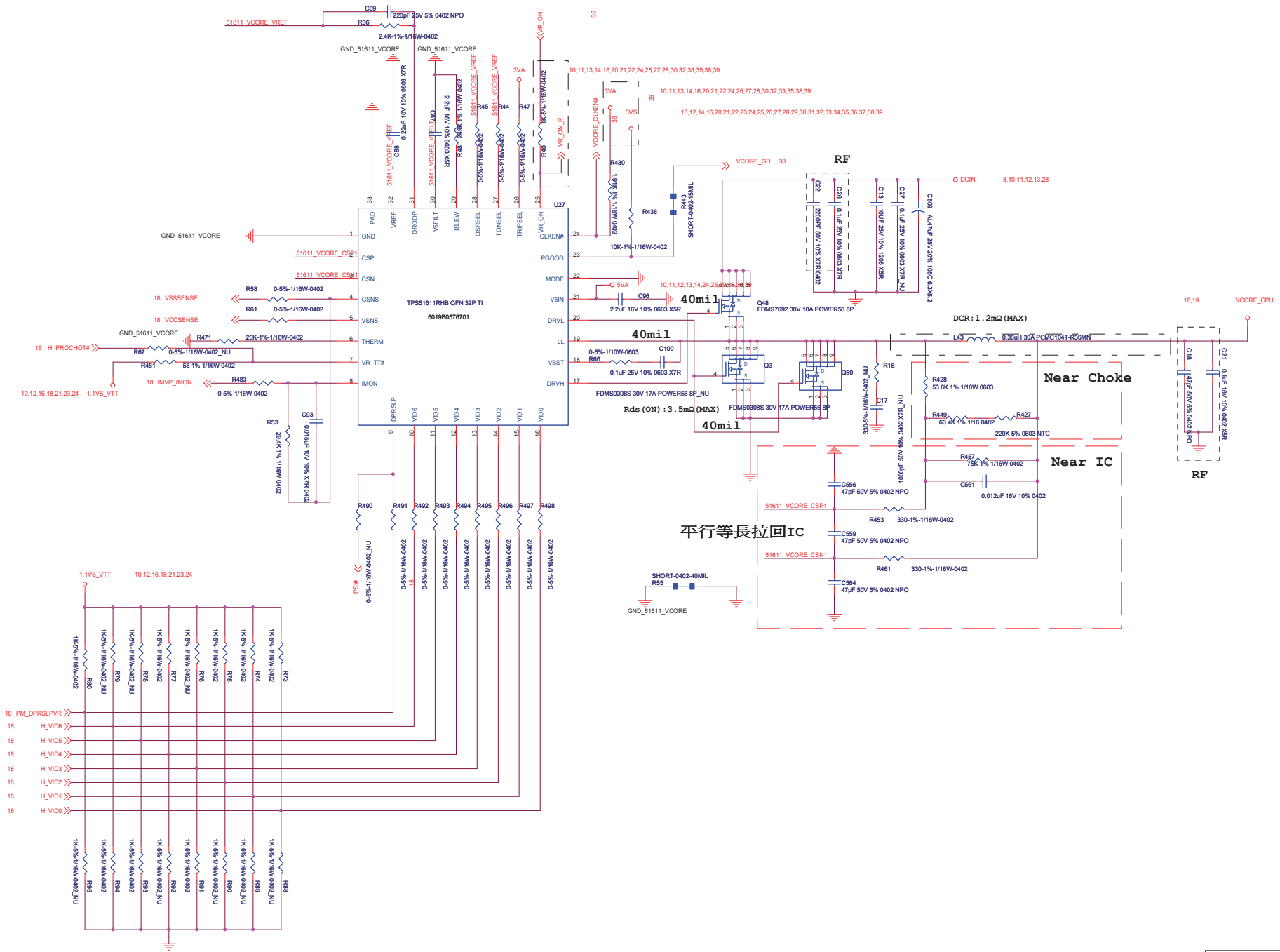
**INVENTEC**

TITLE: **ACER BAP10/BXP10 Adapter in-Charge**

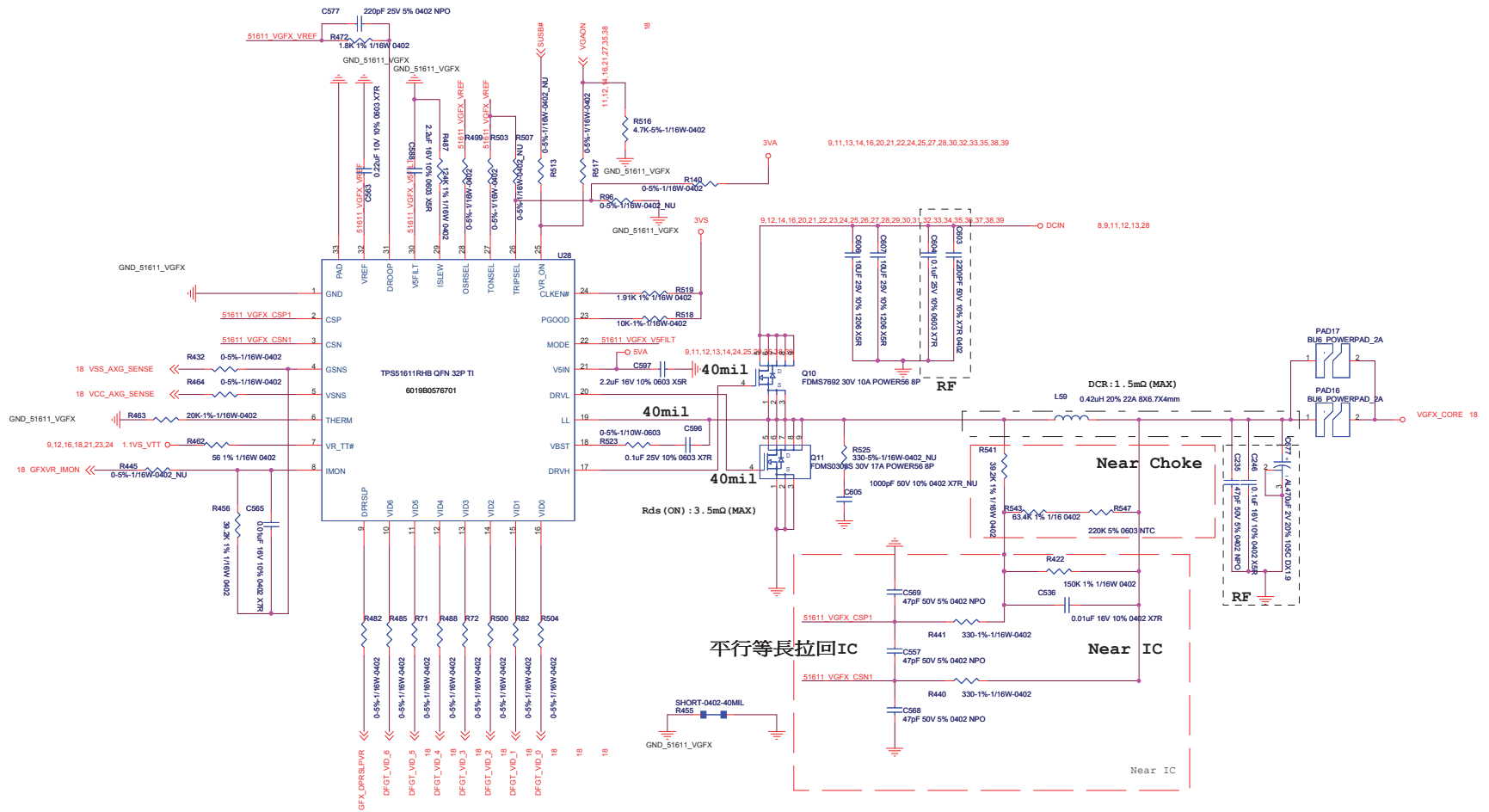
SIZE	CODE	DC NUMBER	REV
Custom	CS	CS-131	A01

CHANGE by: IEC      DATE: Friday, April 02, 2010      SHEET: 8 of 46



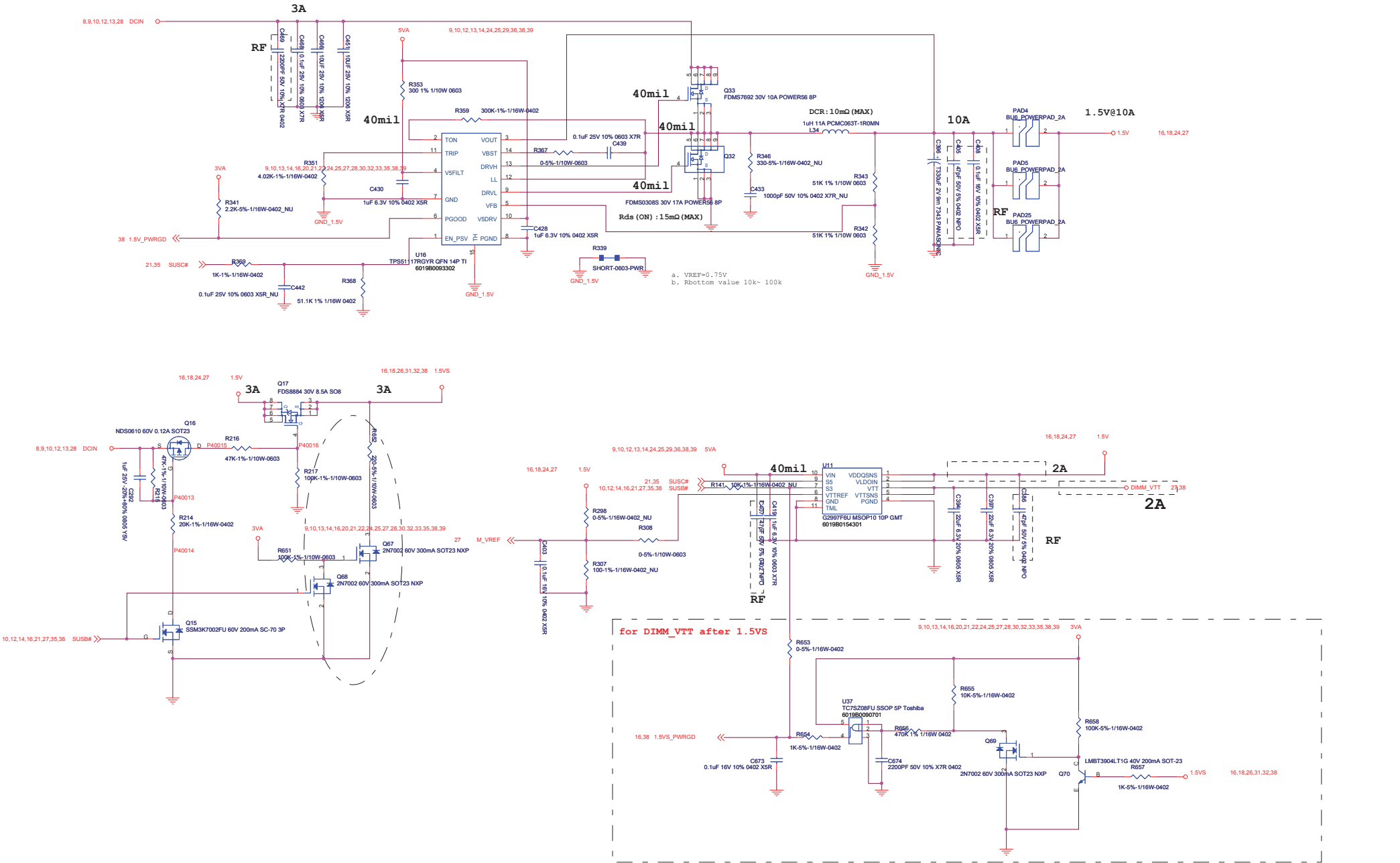


<b>INVENTEC</b>			
TITLE <b>ACER BAPI0/BXP10</b>			
CPU core Power			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
SHEET		9 of 45	



<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b>			
VGFX			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01

# DDR POWER



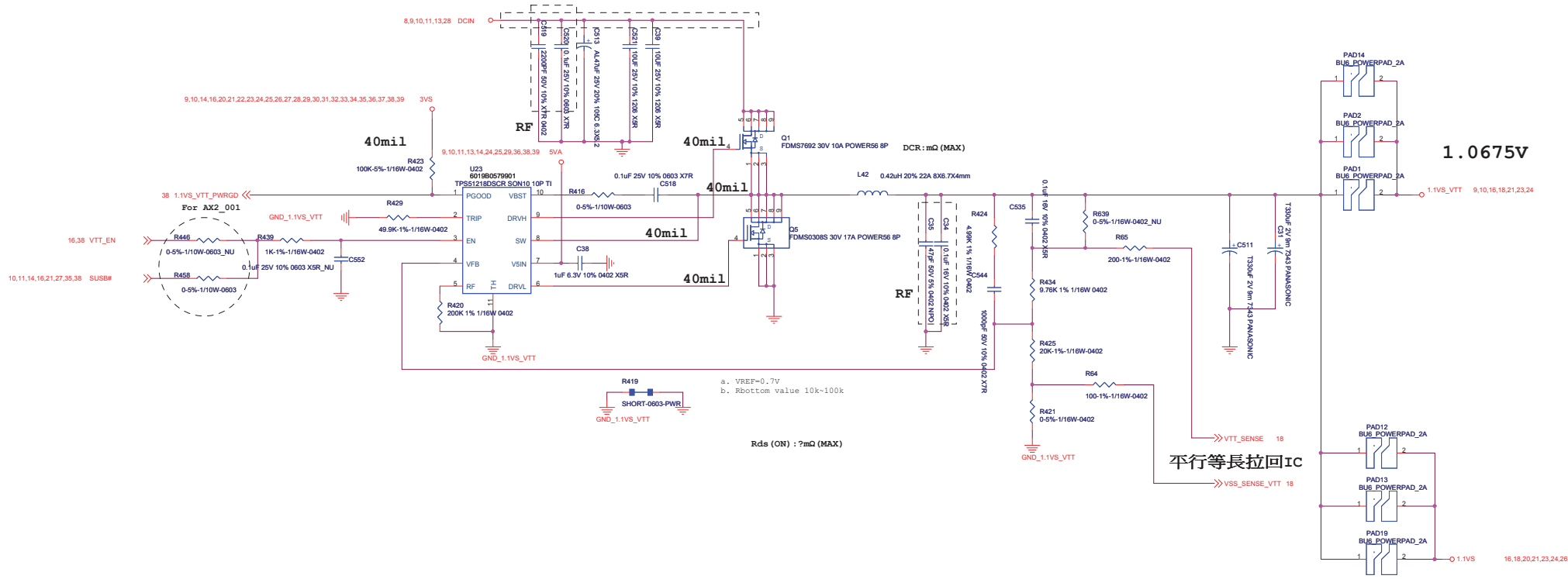
Control by 1.5V

**INVENTEC**

TITLE: ACER BAP10/BXP10  
DDR PWR

SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01

CHANGE by IEG DATE Friday, April 02, 2010 SHEET 11 of 46



1.0675V

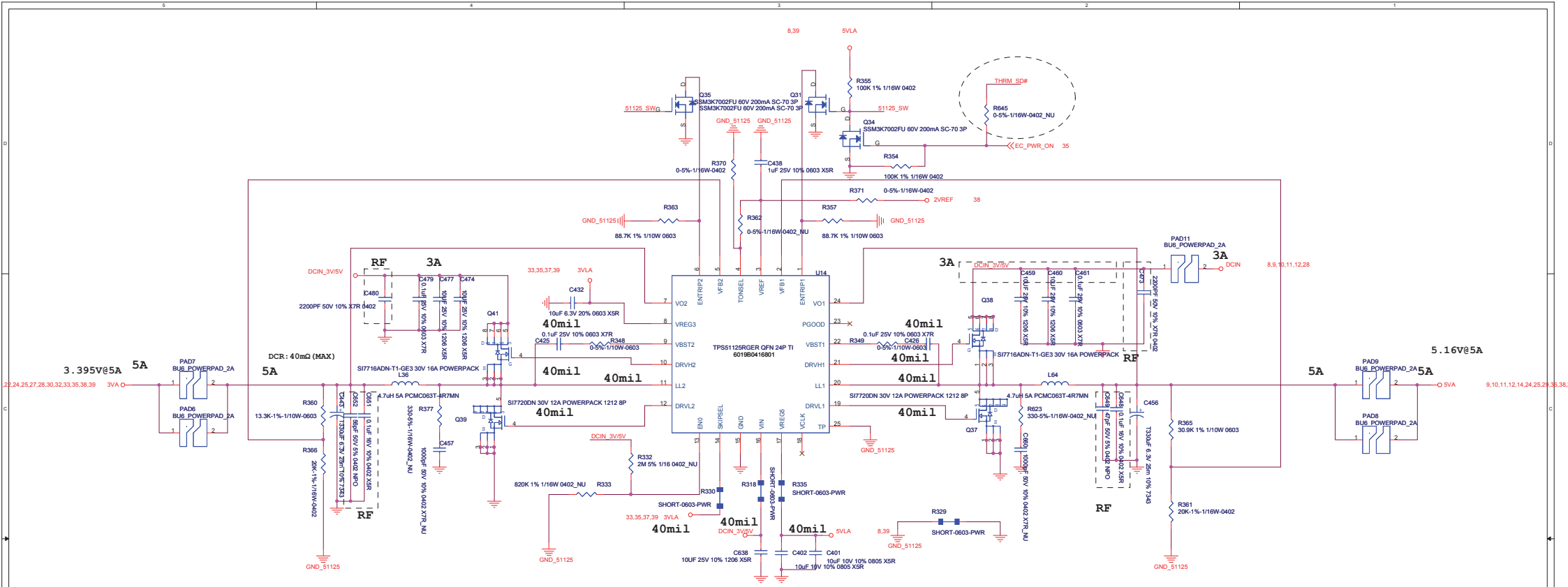
a. VREF=0.7V  
b. Bottom value 10k-100k

Rds (ON) : ?mΩ (MAX)

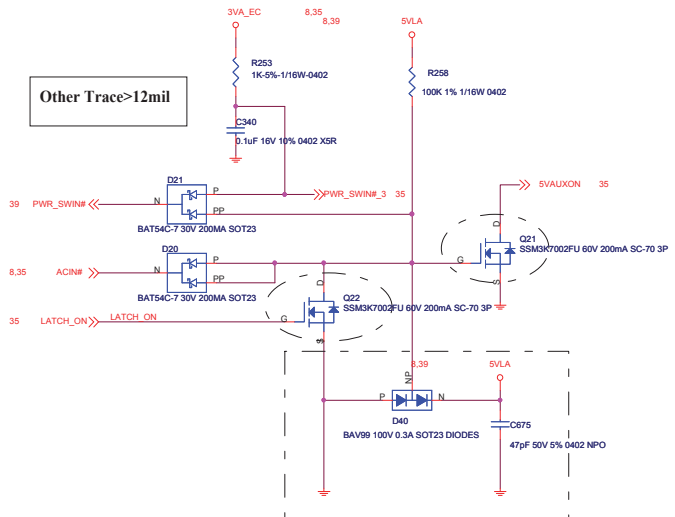
平行等長拉回IC  
VTT\_SENSE 18  
VSS\_SENSE\_VTT 18

# INVENTEC

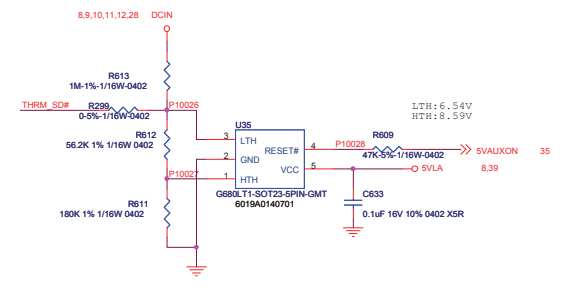
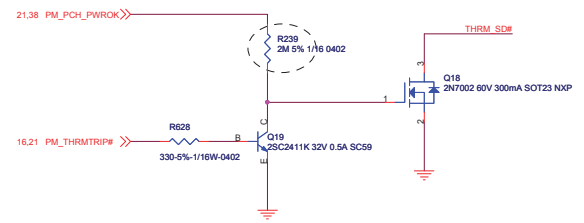
TITLE			
ACER_BAP10/BXP10			
1.IVS_VTT1.IVS			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01



Other Trace>12mil



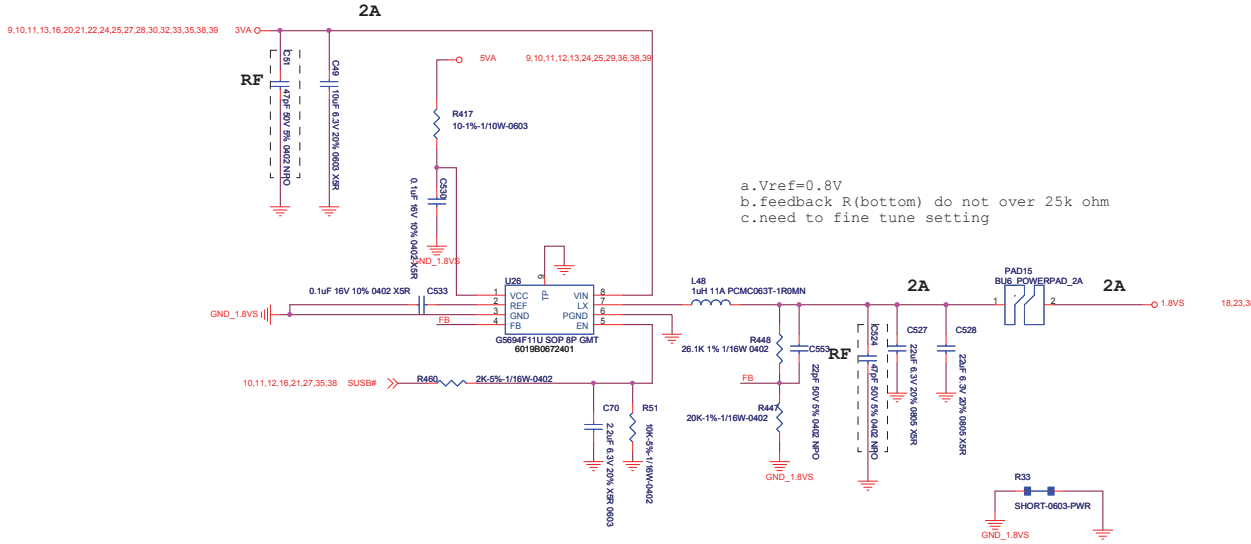
### Temperature Security



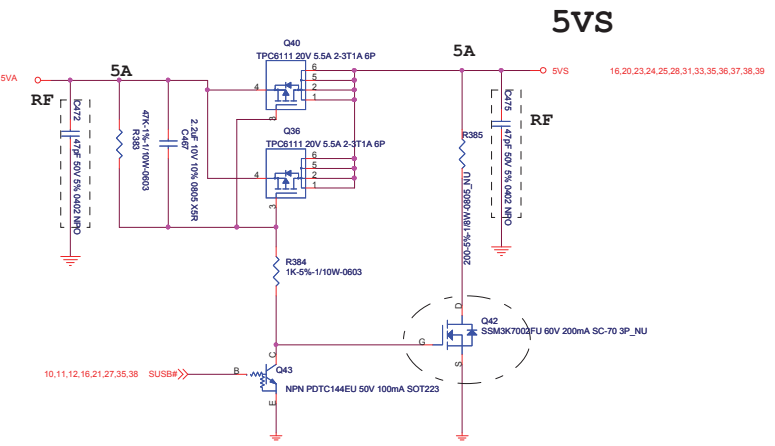
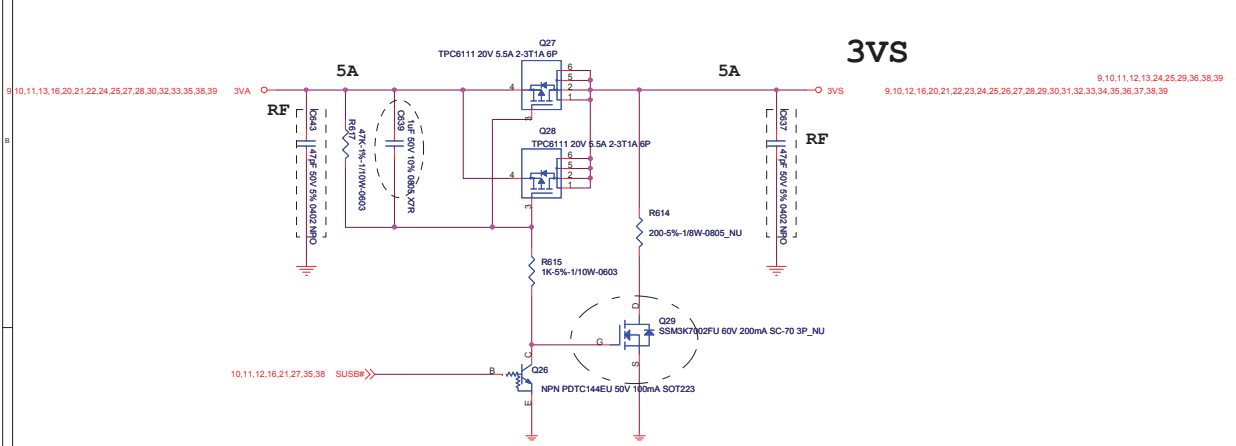
**INVENTEC**

TITLE: ACER\_BAP10/BXP10  
5V\_A5VLA3V\_A3VLA

SIZE	CODE	DC NUMBER	REV
Custom	CS	CS-131	A01



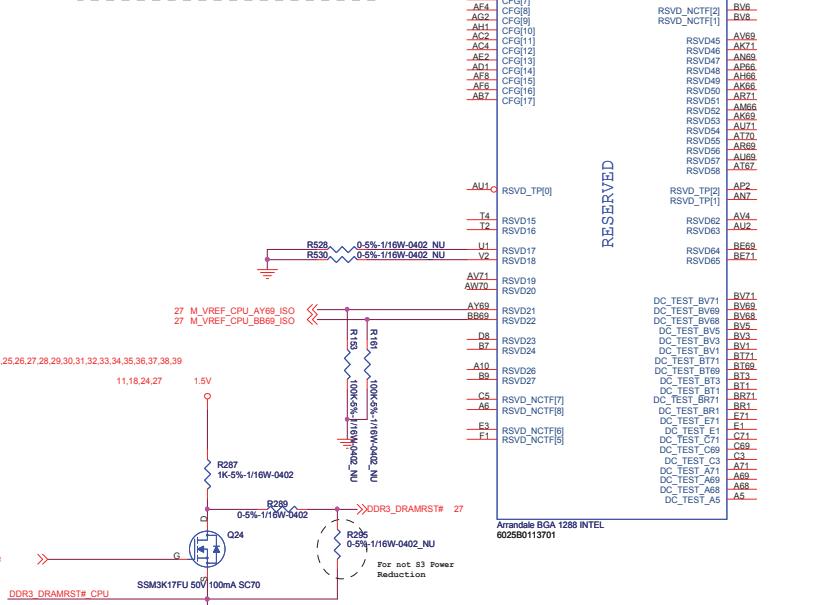
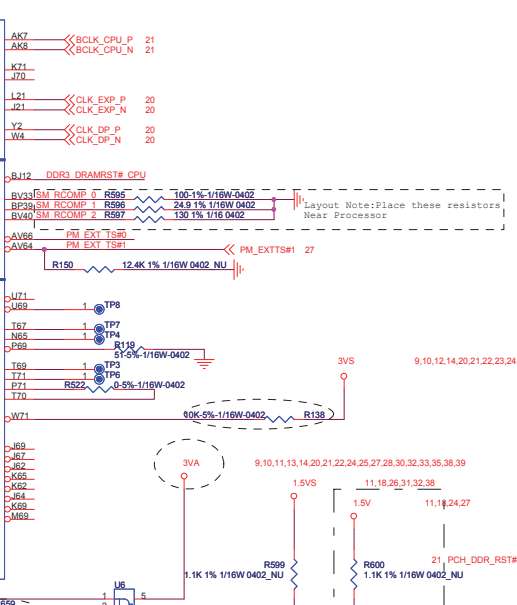
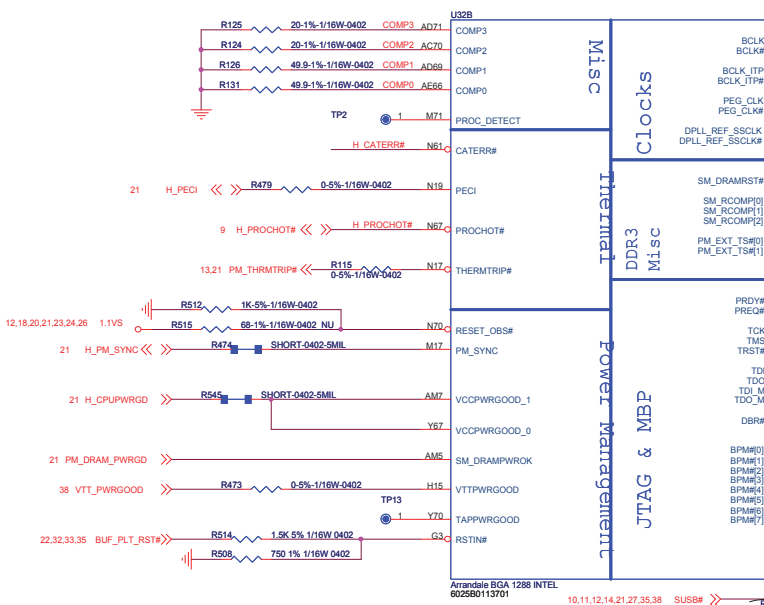
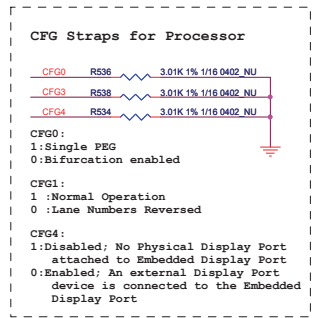
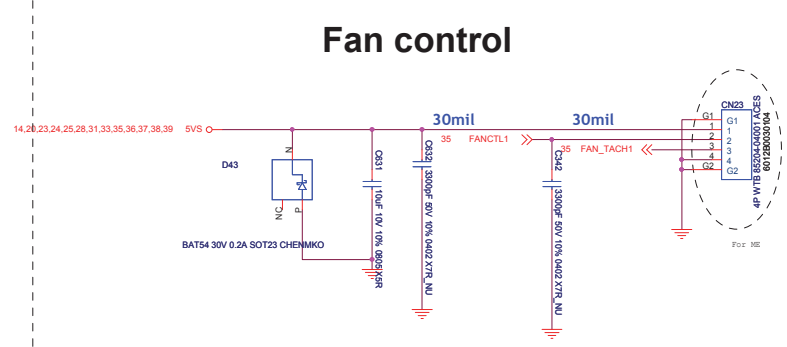
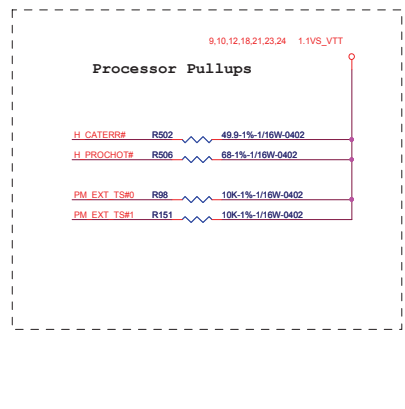
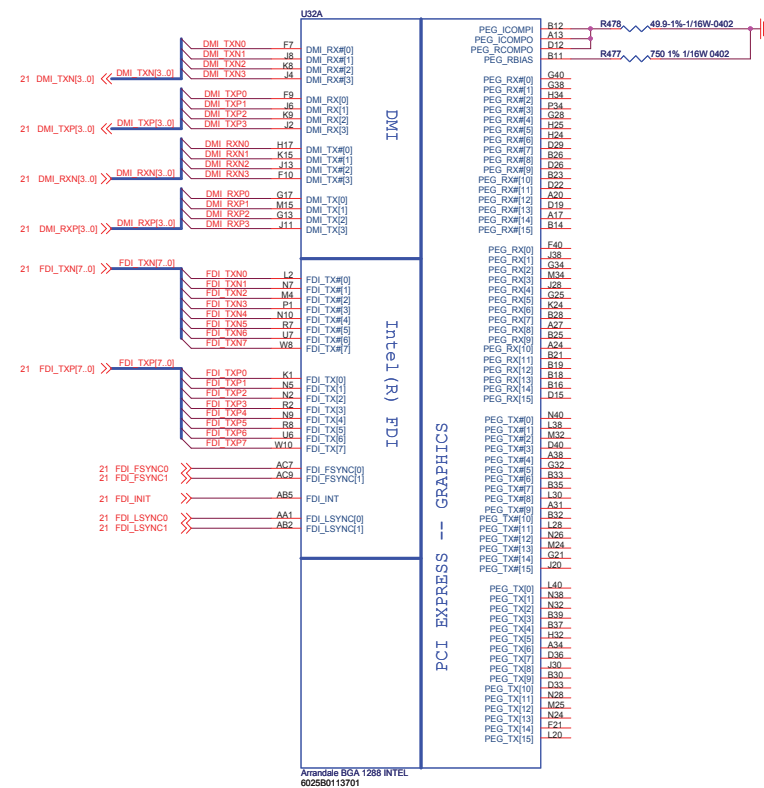
a. Vref=0.8V  
 b. feedback R(bottom) do not over 25k ohm  
 c. need to fine tune setting



<b>INVENTEC</b>			
TITLE			
ACER BAP10/BXP10			
3VS/5VS/1.8VS/3VM			
SIZE	CODE	IPC NUMBER	REV
Custom	CS	CS-131	A01

Blank

Title	
ACER_BAP10IBXP10	
Size	Document Number
Customer/Doc#	Rev
Date: Friday, April 02, 2010	Sheet 15 of 45



**INVENTEC**

TITLE: **ACER BAP10/BXP10**

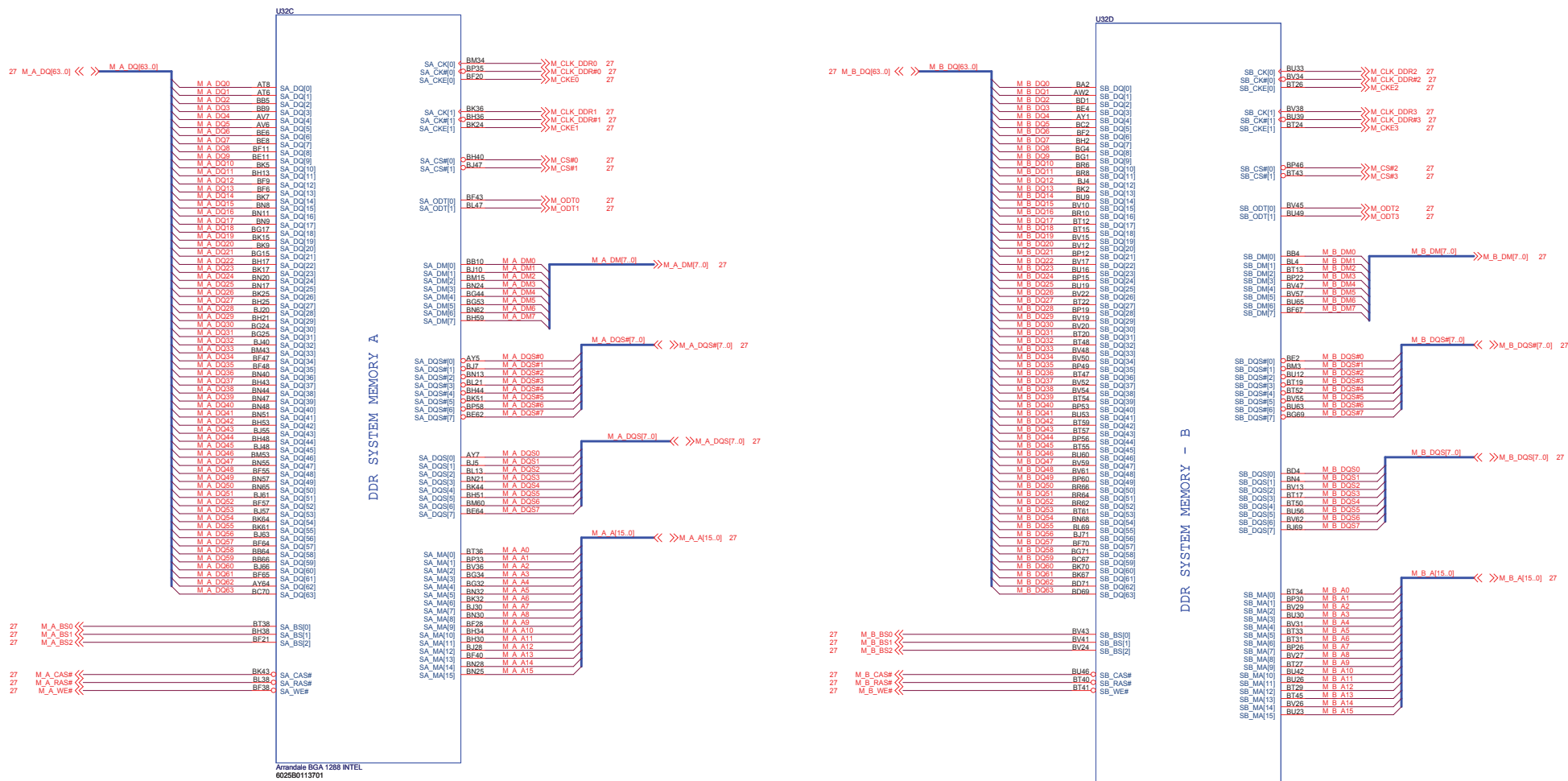
Processors: (1/4)

SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01

CHANGE By: IEC DATE: Friday, April 02, 2010

SHEET 16 of 45



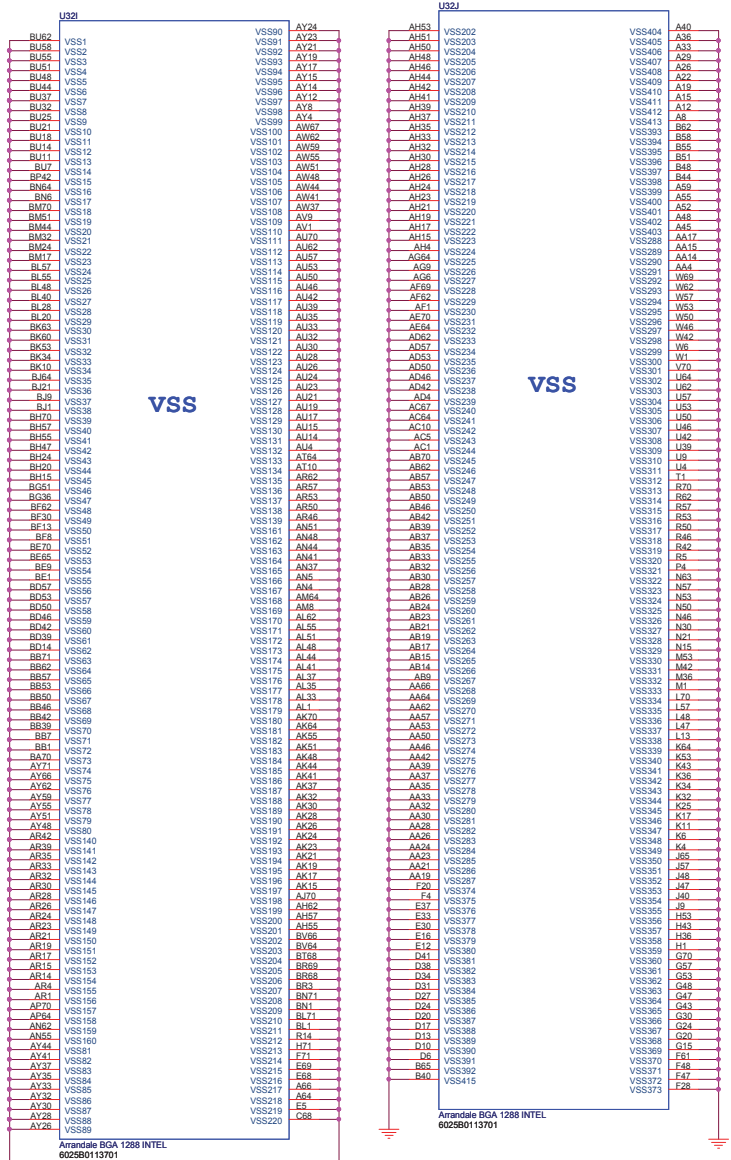


Arrandale BGA 1288 INTEL  
602580113701

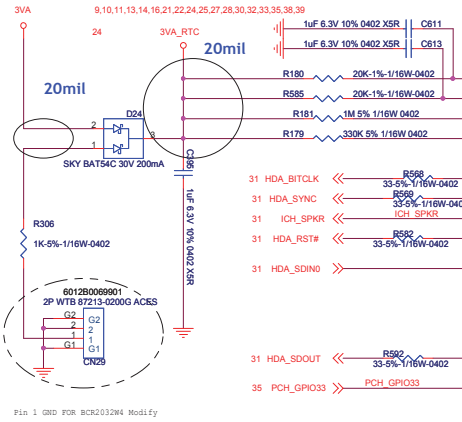
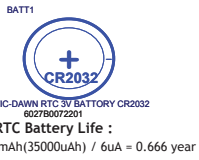
Arrandale BGA 1288 INTEL  
602580113701

<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b> Processor (24)			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
CHANGE By	IEC	DATE	Friday, April 02, 2010
SHEET		17 of 45	

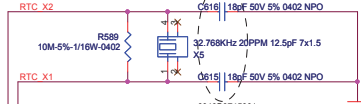




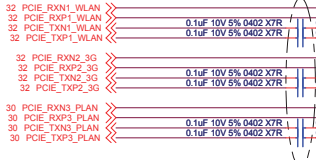
# RTC Circuit



Pin 1 GND FOR BCR2032M4 Modify



# WLAN 3G CARD PCI E LAN



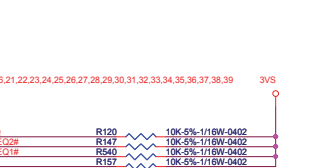
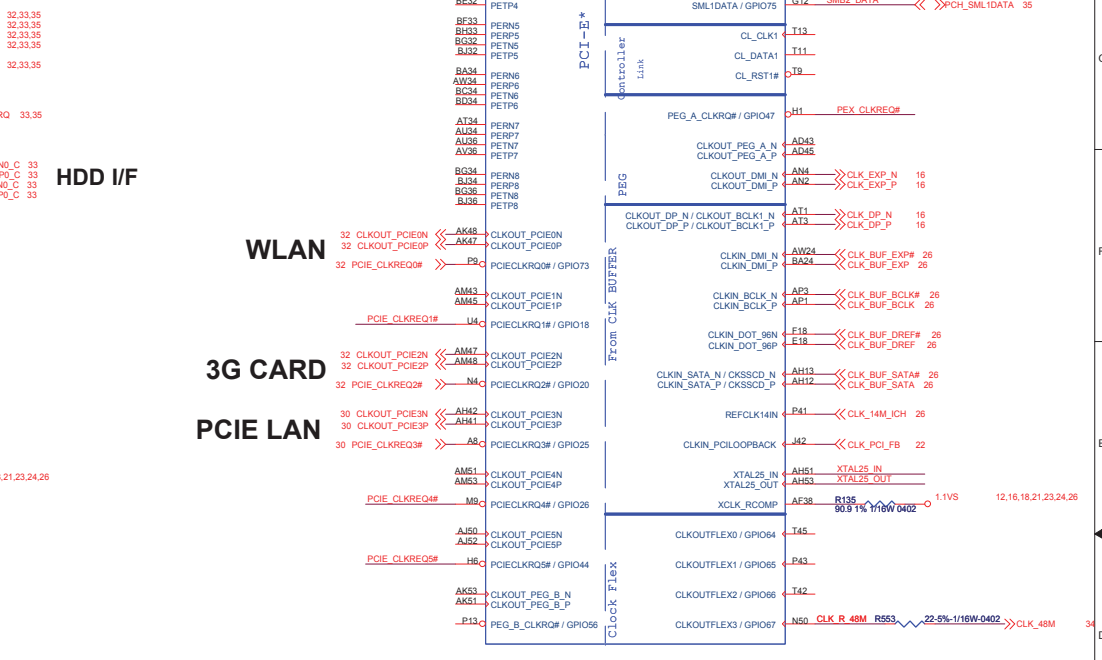
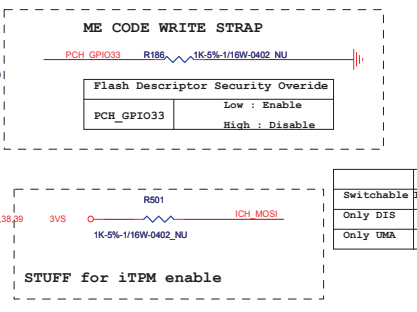
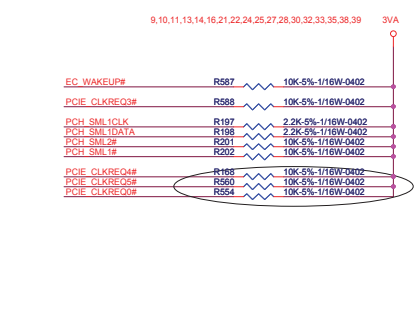
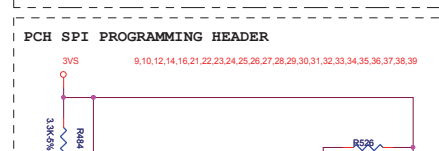
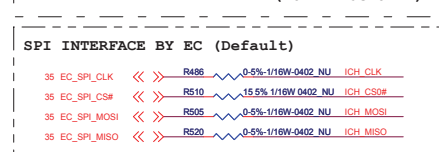
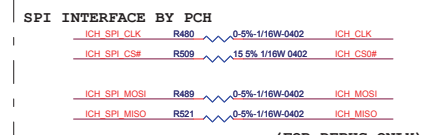
# HDD I/F



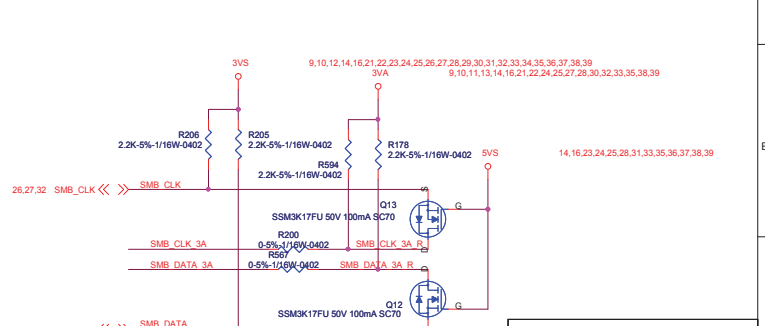
# WLAN



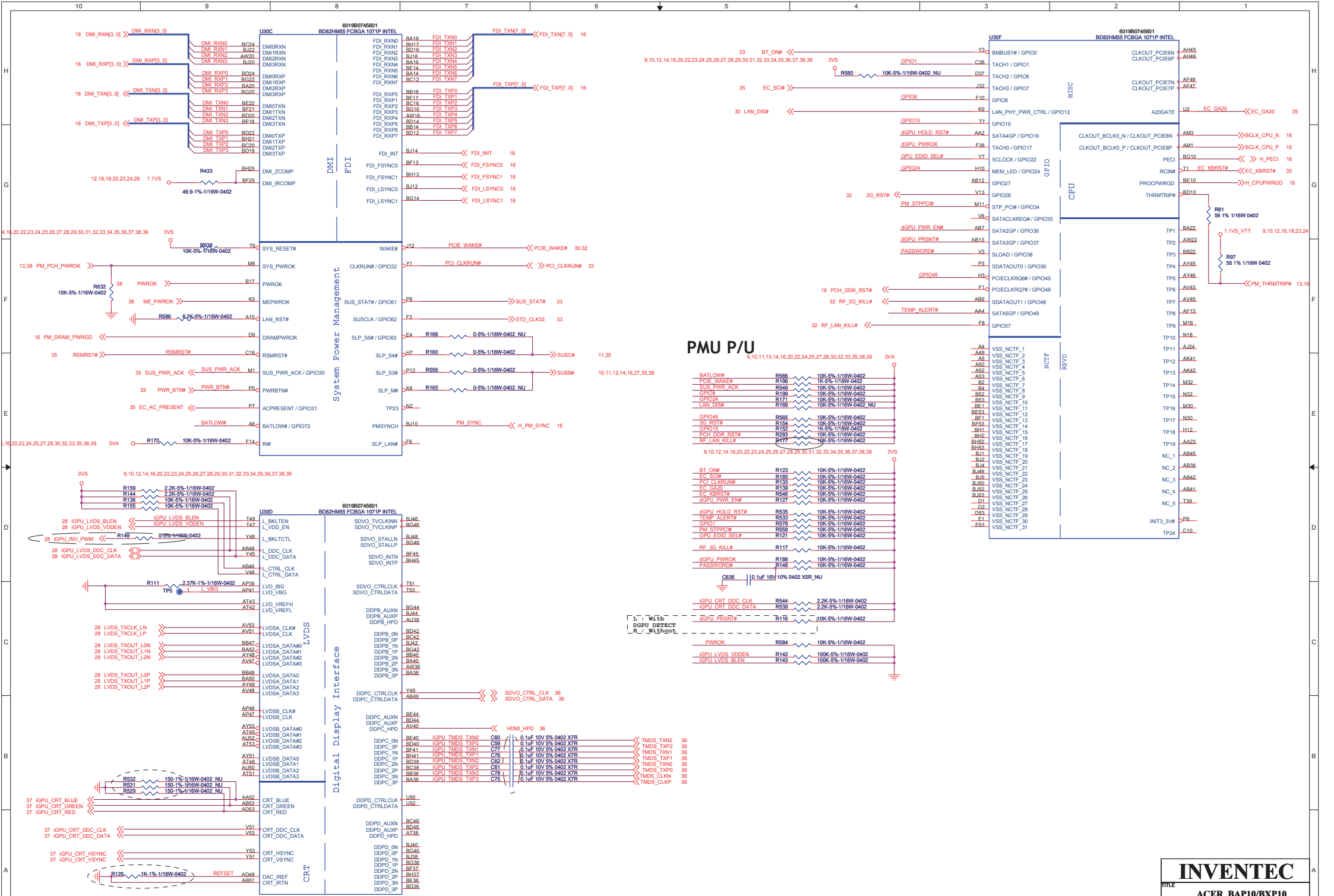
# 3G CARD PCI E LAN



PCIECLKREQ# 0,3,4,5,6 --- 3VA plane  
PCIECLKREQ# 1,2 --- 3VS plane



<b>INVENTEC</b>			
TITLE ACER BAPI0/BXPI0 PCH(HDA,JTAG,SATA)			
SIZE Custom	CODE CS	DWG NUMBER CS-131	REV A01
SHEET 20		of 45	

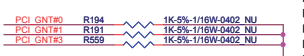
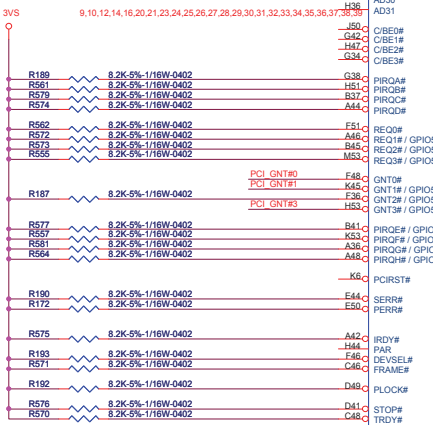


**PMU P/U**

BATLOW#	R566	10K-5%-1/16W-0402
PCIE_WAKE#	R196	1K-5%-1/16W-0402
SUS_PWR_ACK	R649	10K-5%-1/16W-0402
GPIO8	R199	10K-5%-1/16W-0402
GPIO24	R171	10K-5%-1/16W-0402
LAN_DIS#	R169	10K-5%-1/16W-0402 NU
GPIO4#	R565	10K-5%-1/16W-0402
3G_RST#	R154	10K-5%-1/16W-0402
GPIO15	R182	1K-5%-1/16W-0402
PCH_DDR_RST#	R263	10K-5%-1/16W-0402
RF_LAN_KILL#	R177	10K-5%-1/16W-0402
BT_ON#	R123	10K-5%-1/16W-0402
EC_SC#	R185	10K-5%-1/16W-0402
PCI_CLKRUN#	R133	10K-5%-1/16W-0402
EC_GA20	R139	10K-5%-1/16W-0402
EC_KBRST#	R546	10K-5%-1/16W-0402
GPU_PWR_EN#	R127	10K-5%-1/16W-0402
GPU_HOLD_RST#	R535	10K-5%-1/16W-0402
TEMP_ALERT#	R533	10K-5%-1/16W-0402
GPIO1	R578	10K-5%-1/16W-0402
PM_STPPC#	R558	10K-5%-1/16W-0402
GPU_EDID_SEL#	R121	10K-5%-1/16W-0402
PASSWD#	R188	10K-5%-1/16W-0402
GPU_PWR_OK	R148	10K-5%-1/16W-0402
GPIO15	R544	2.2K-5%-1/16W-0402
GPU_CRT_DDC_CLK	R544	2.2K-5%-1/16W-0402
GPU_CRT_DDC_DATA	R539	2.2K-5%-1/16W-0402
GPU_PRESNT#	R116	10K-5%-1/16W-0402
PWR_OK	R584	10K-5%-1/16W-0402
GPU_LVDS_VDDEN	R142	100K-5%-1/16W-0402
GPU_LVDS_BLEN	R143	100K-5%-1/16W-0402

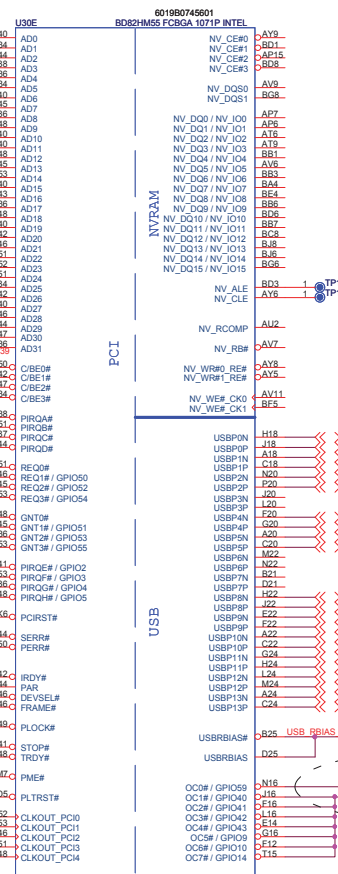
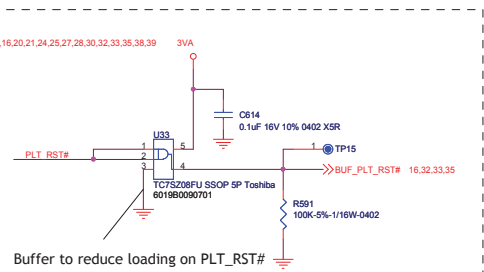
<b>INVENTEC</b>			
<b>ACER BAP10/BXP10</b>			
<b>PCH(FDL/MLSPM)</b>			
SIZE	CODE	DOC NUMBER	REV
Custom	CS-131		A01
SHEET	21	of	45

### PCI Pull up



PCI_GNT#3	No stuff - by default Stuff : For A16 swap override	
PCI_GNT#0	PCI_GNT#1	
0	LPC	
Floating	0	PCI
Floating	Floating	SPI

### BIOS type select



5/5 mils spacing on microstrip

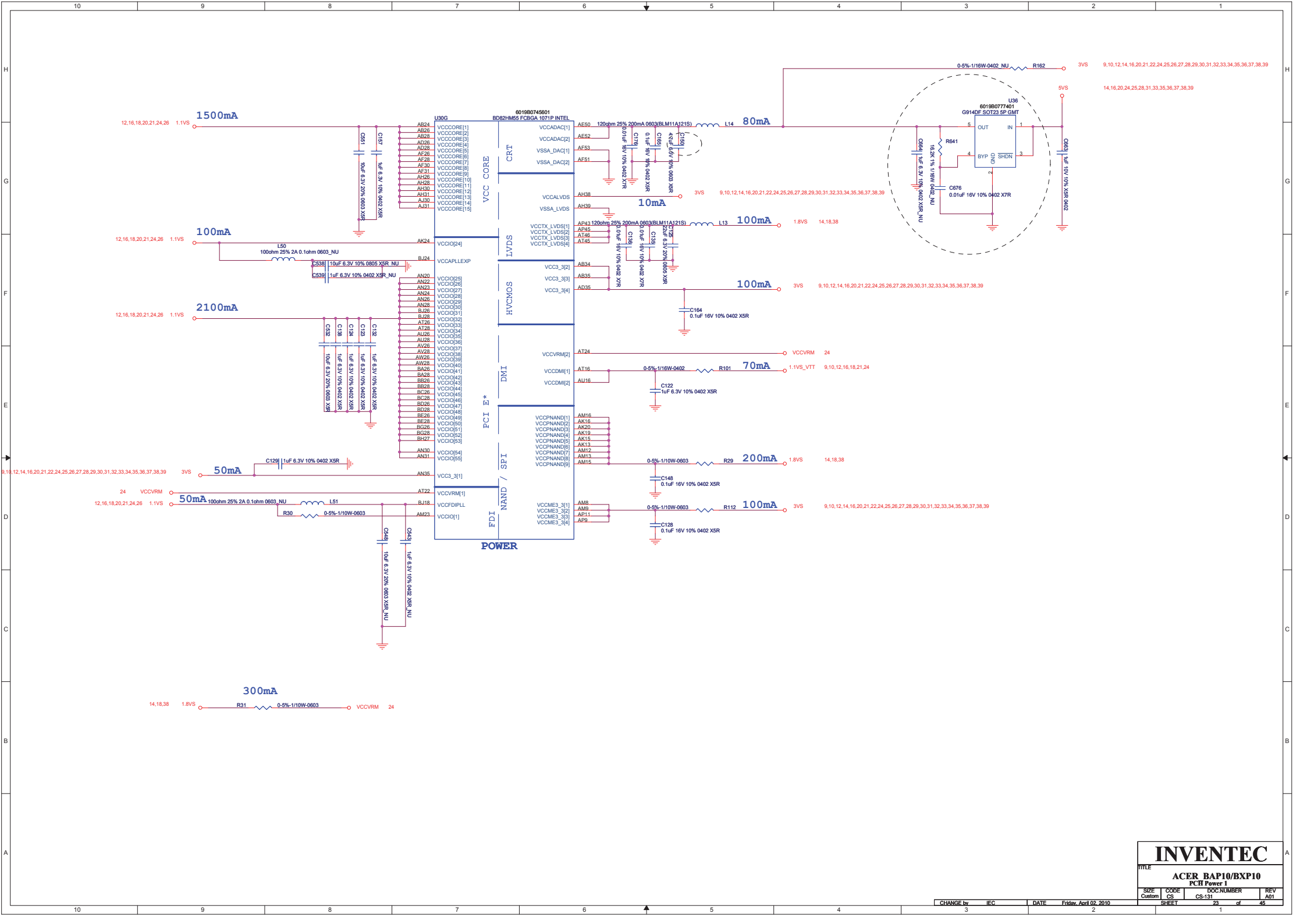
### BIOS ID setting

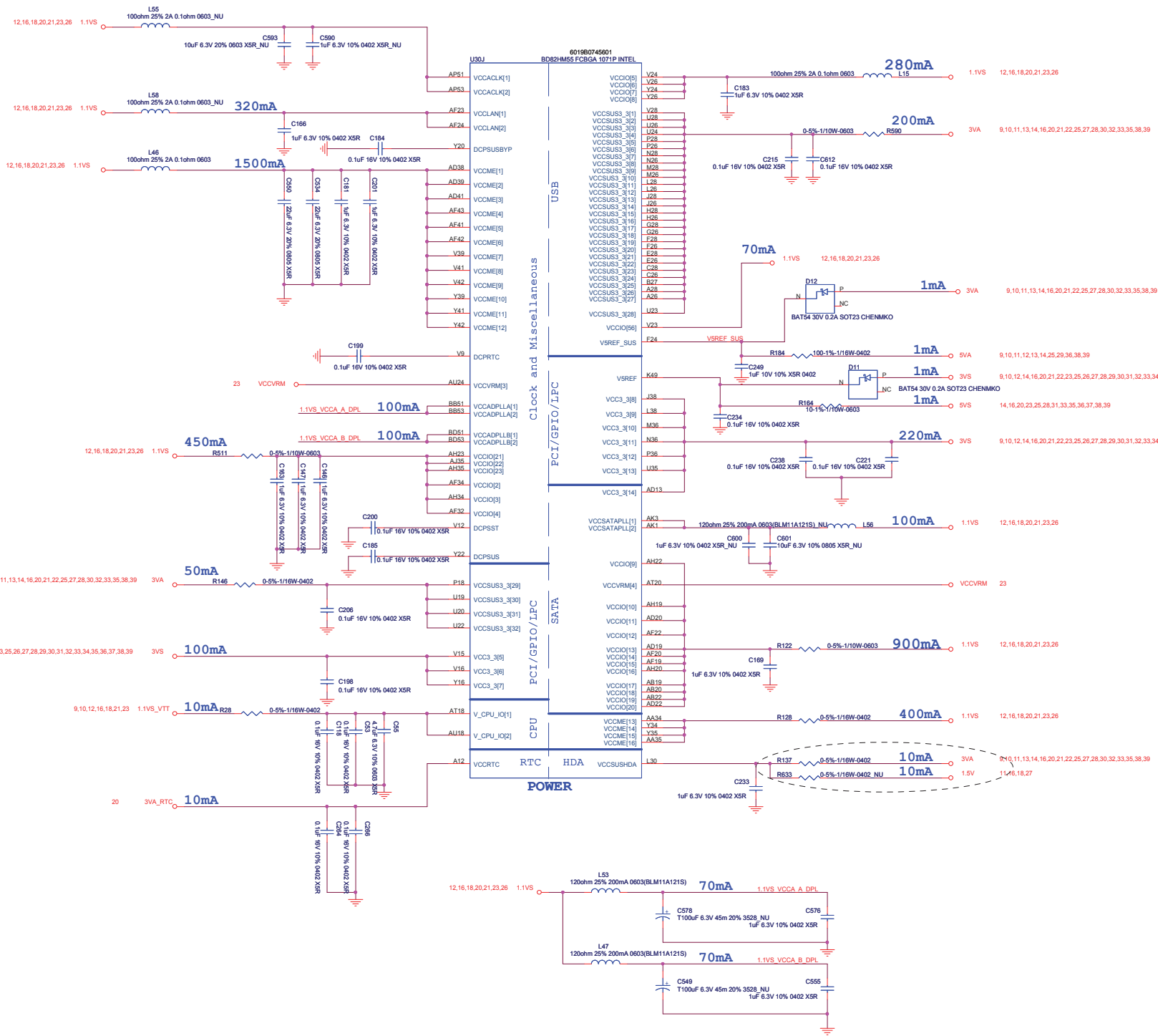
Project	MB_ID4	MB_ID3	MB_ID2	MB_ID1	MB_ID0
BAP10 (UMA)	1	1	1	1	1
BXP10 (UMA)	1	1	1	1	0
BAP30 (UMA)	1	1	1	0	1
BXP30 (UMA)	1	1	1	0	0
BAD50 (UMA)	1	1	0	1	1
BXD50 (UMA)	1	1	0	1	0
SJM40 (UMA)	1	1	0	0	1
SJM40 (dGPU)	1	1	0	0	0
BAP30 (dGPU)	1	0	1	1	1
BAD50 (dGPU)	1	0	1	0	1
BXD50 (dGPU)	1	0	1	0	0
SJM40 (dGPU optimum)	1	0	0	1	1
BAP30 (dGPU optimum)	1	0	0	0	1
BXP30 (dGPU optimum)	1	0	0	0	0

**INVENTEC**

TITLE: **ACER BAP10/BXP10 PCH(USB, PCI)**

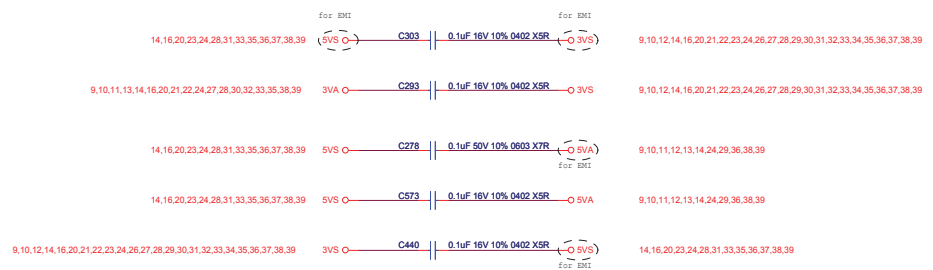
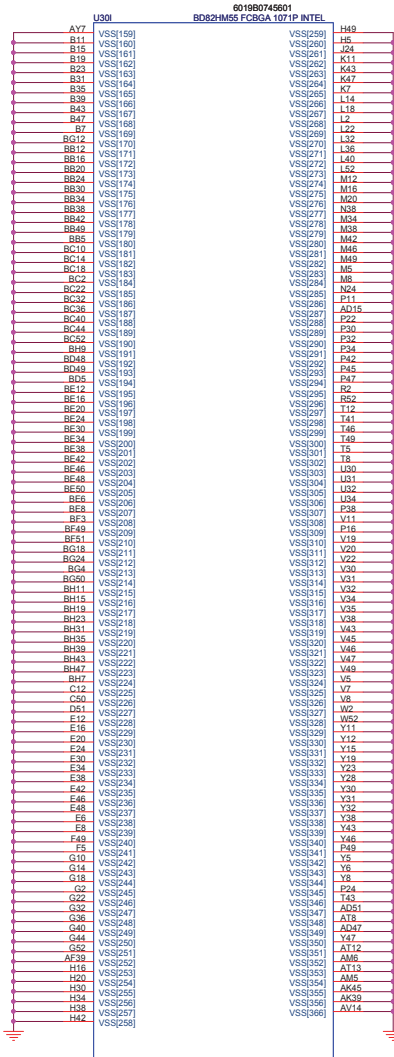
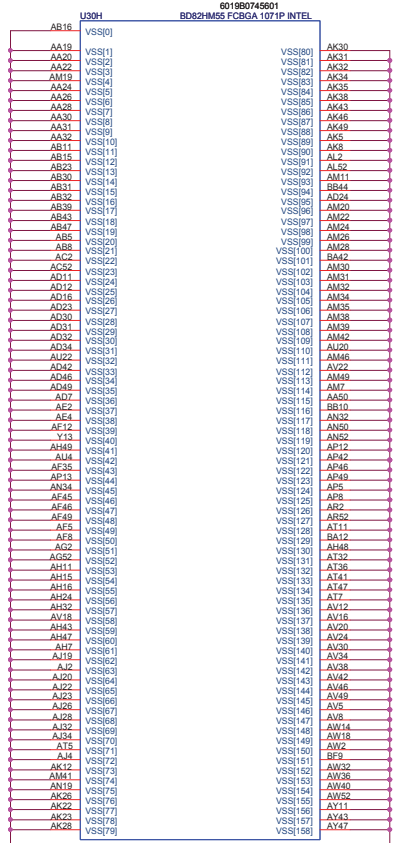
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
SHEET 22		of 46	





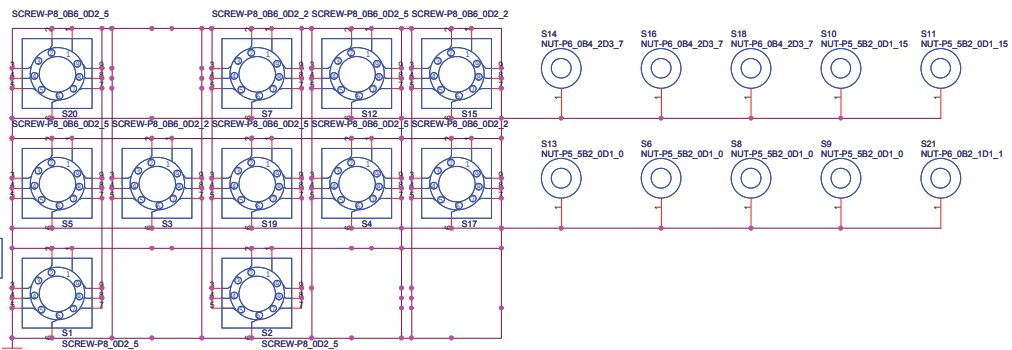
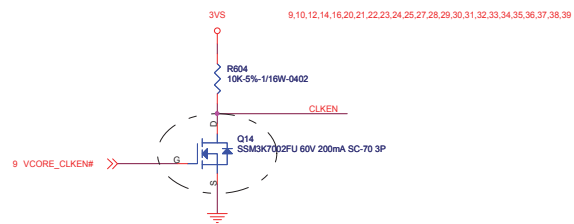
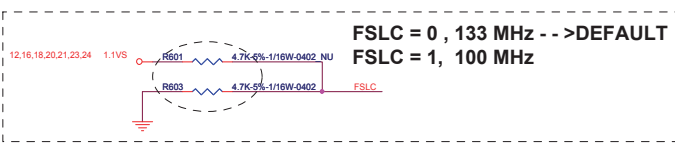
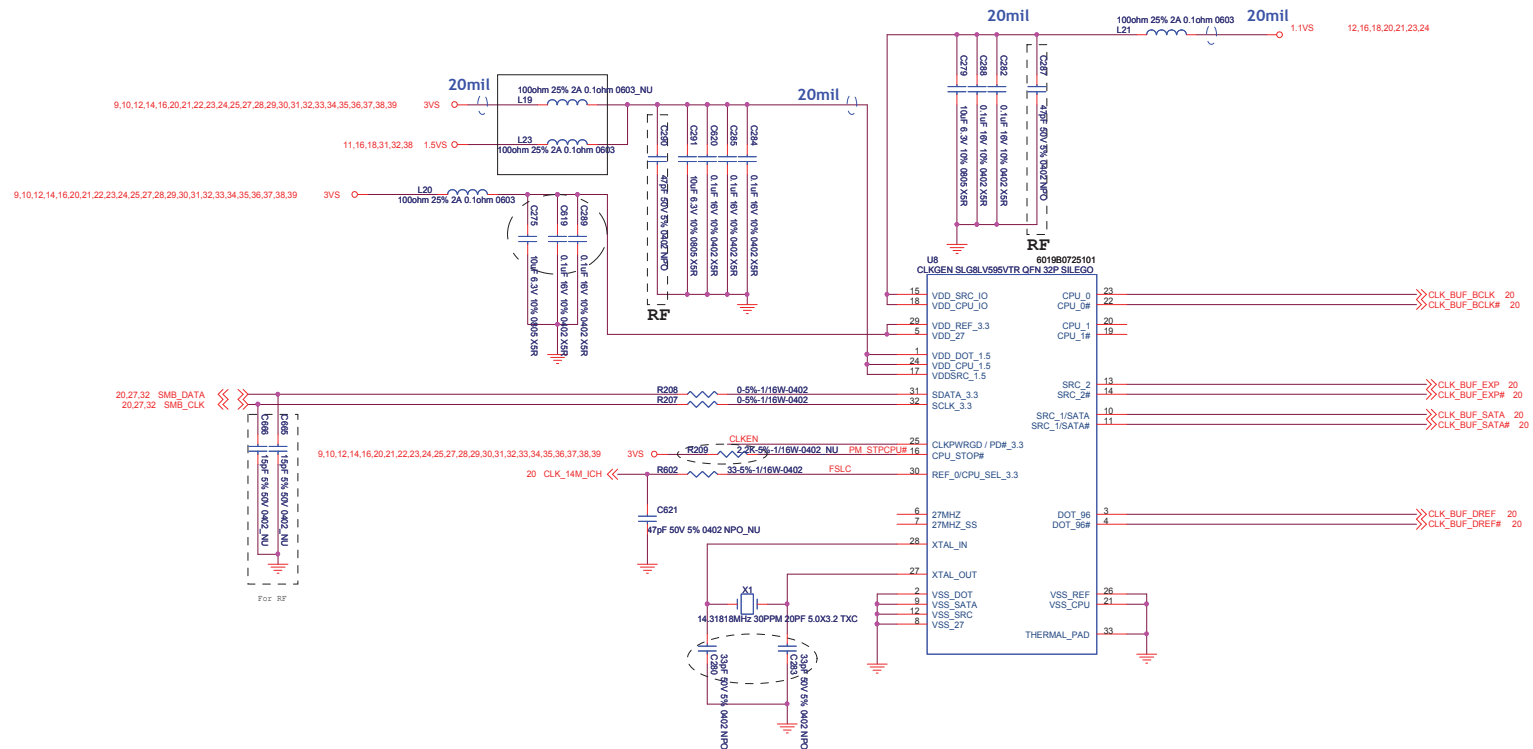
<b>INVENTEC</b>			
ACER BAPI0/BXP10			
PCH Power 2			
SIZE	CODE	DWG NUMBER	REV
Custom	CS	CS-131	A01
SHEET		24	of 45





FOR EMI Suggest

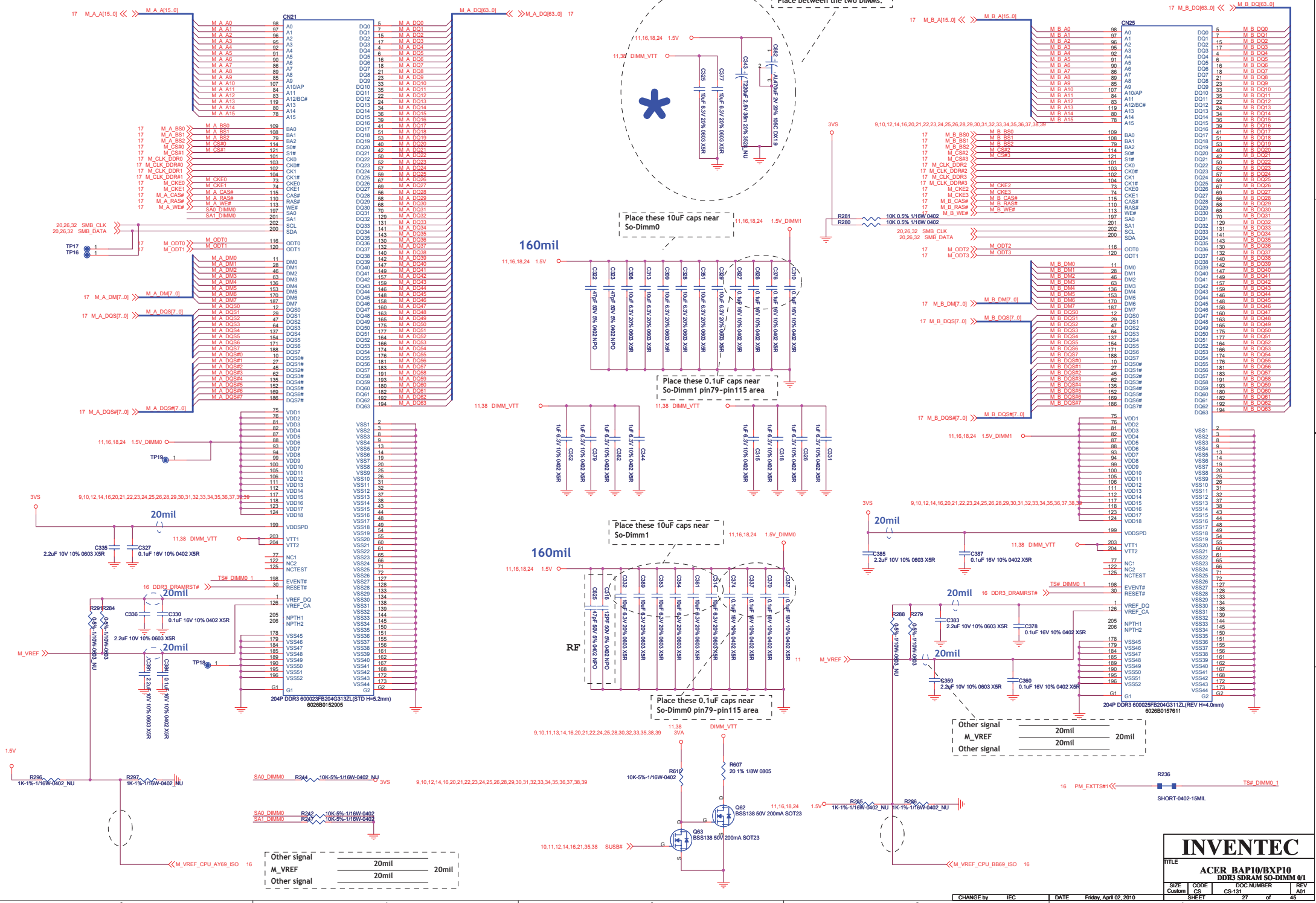
<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b>			
PCH Power 2			
SIZE Custom	CODE CS	DWG NUMBER CS-131	REV A01
SHEET		25	46

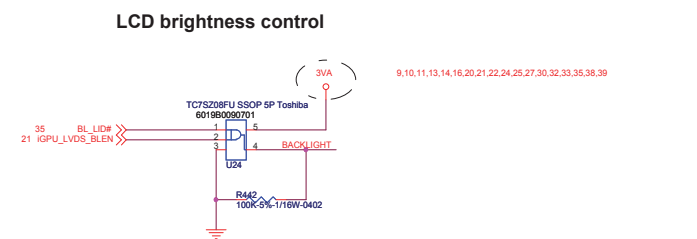
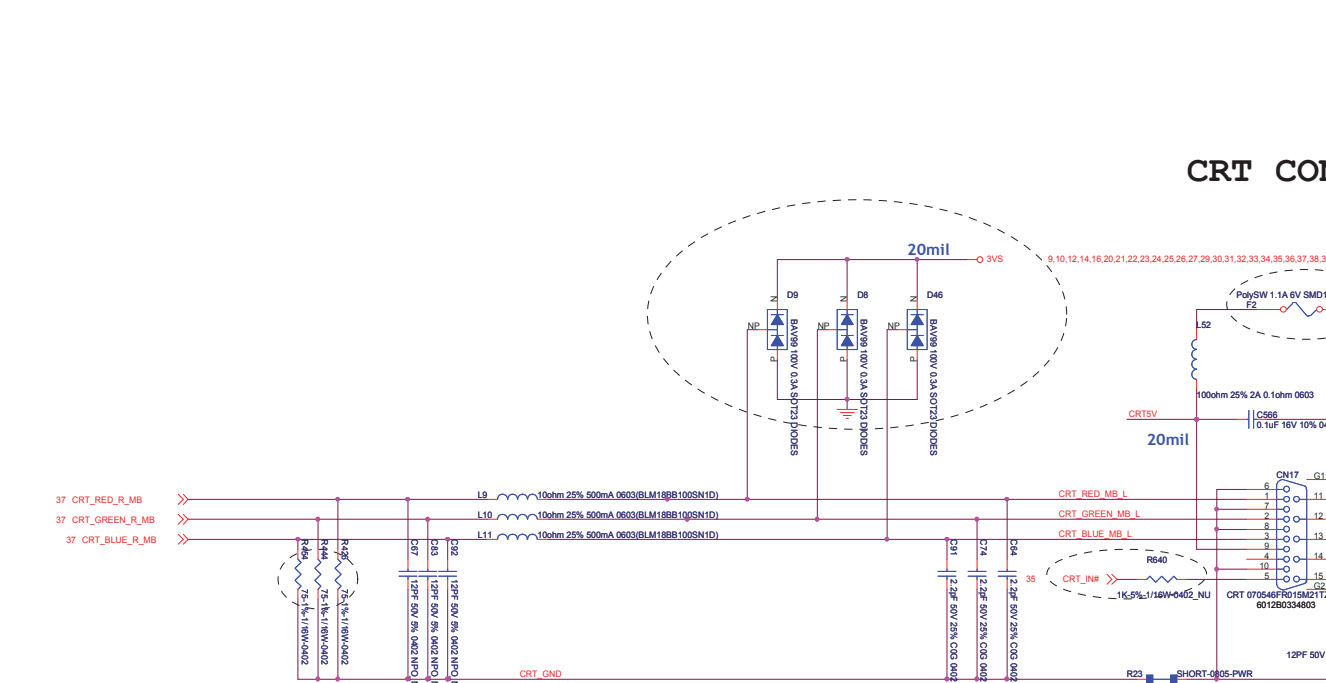
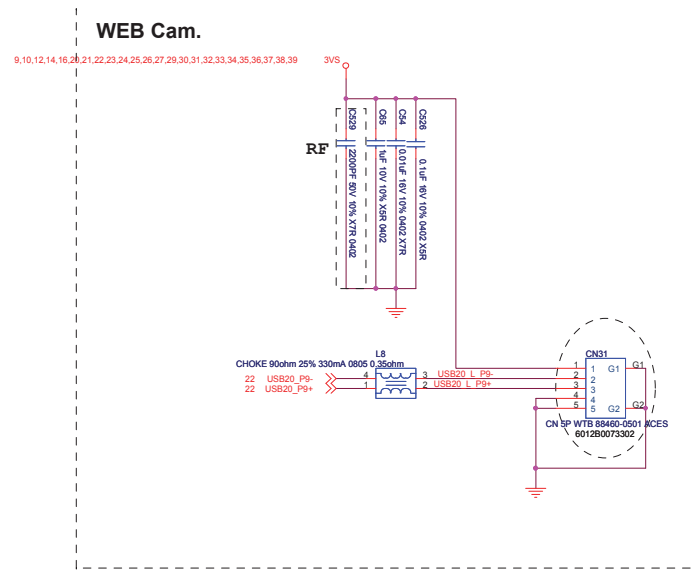
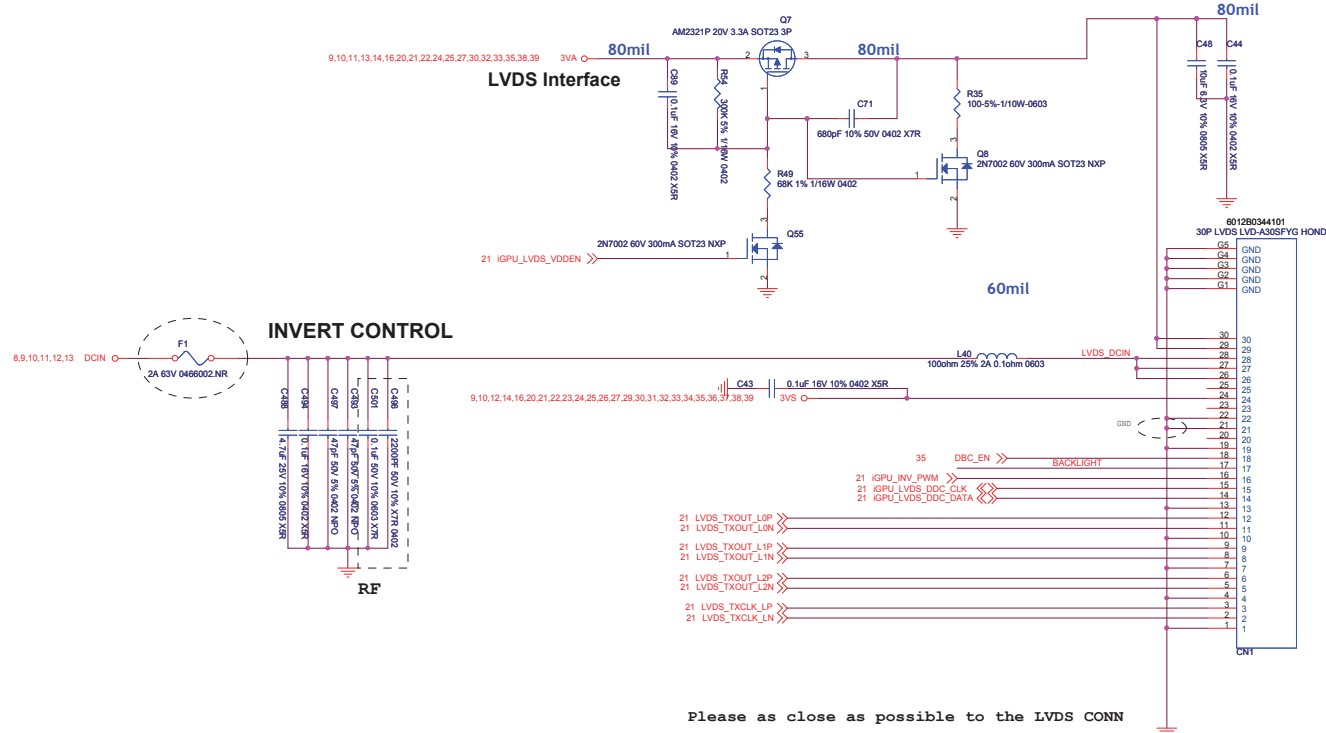


<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b>			
ClkGen Generator			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01
SHEET		26 of 45	

# SO-DIMM0

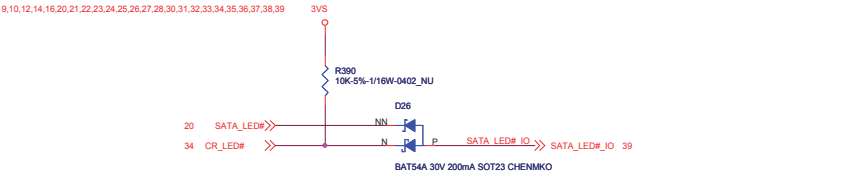
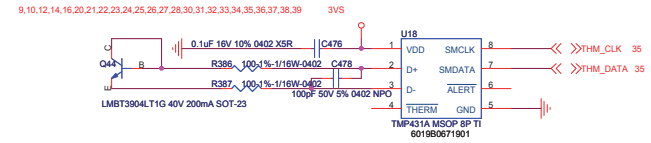
# SO-DIMM1



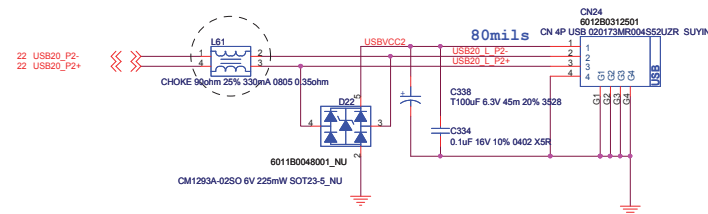
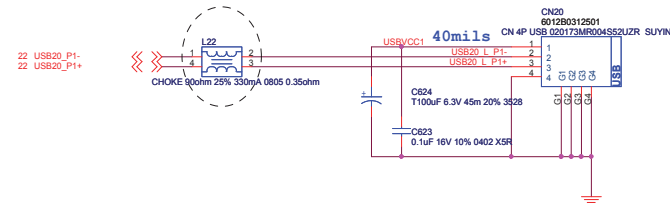
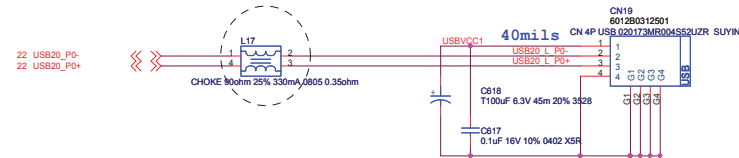
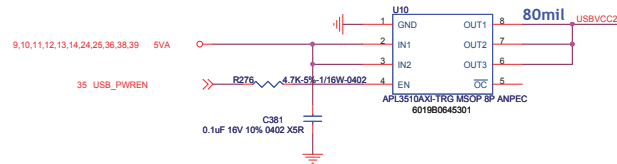
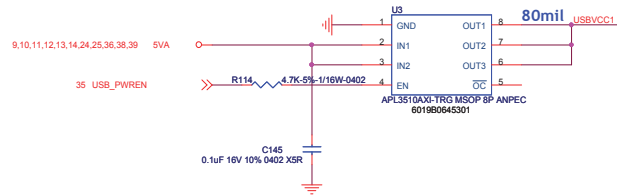


# Thermal Sensor

**REMOTE thermal sensor**  
Place near the hottest spot area under Palm-rest



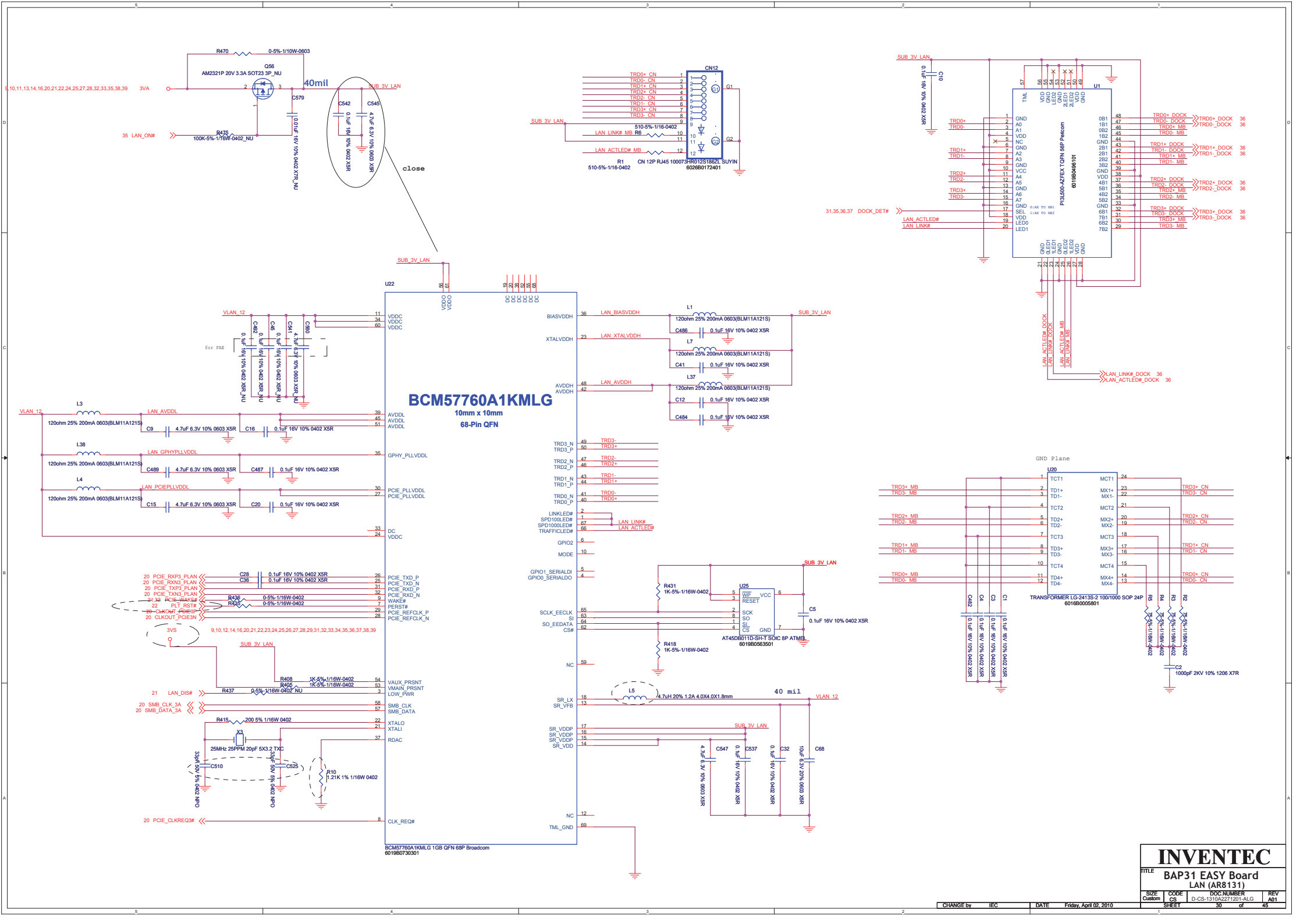
# USB



**INVENTEC**

TITLE  
**ACER BAP10/BXP10**  
USBSSENSOR

SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
----------------	------------	----------------------	------------

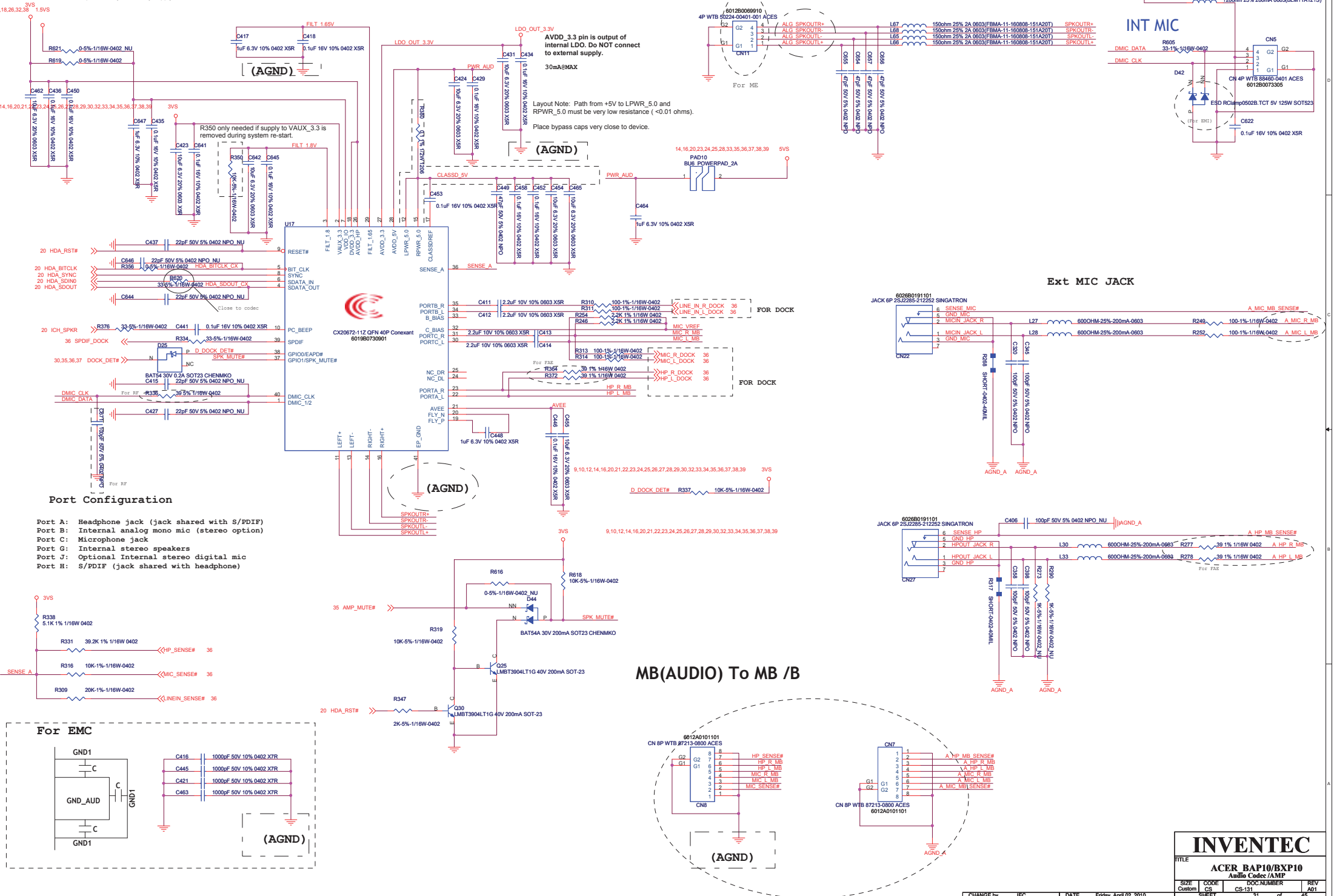


**Note:**  
To support Wake-on-Jack, the CODEC VAUX\_3.3 pins must be powered by a Standby supply.

# AUDIO CODEC

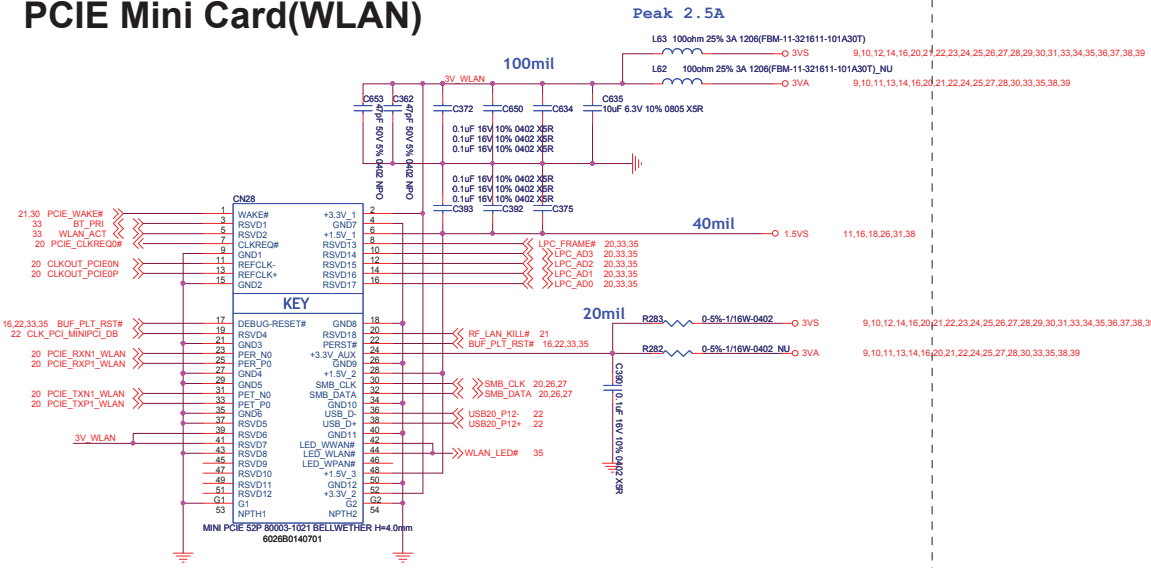
# SPEAKER 30mil

9,10,12,14,16,20,21,22,23,24,25,26,27,28,29,30,32,33,34,35,36,37,38,39 3VS



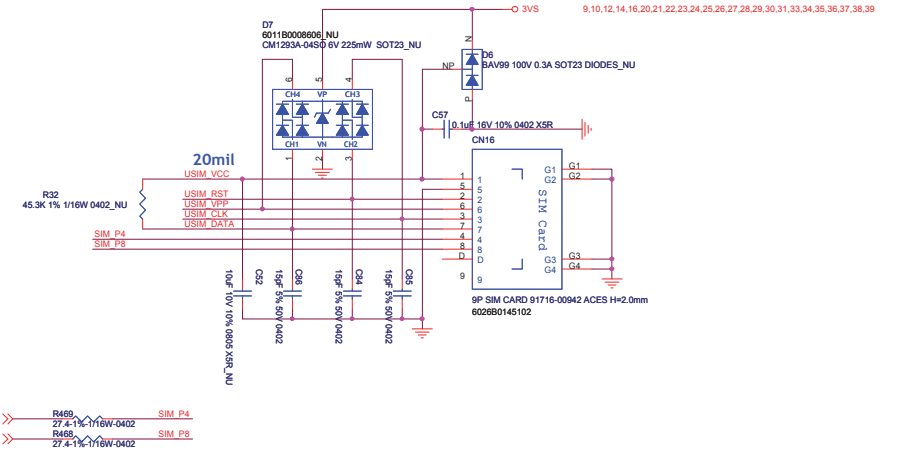
<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b>			
Audio Codec / AMP			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
CHANGE By IEC		DATE Friday, April 02, 2010	
SHEET 31		of 45	

# PCIE Mini Card(WLAN)

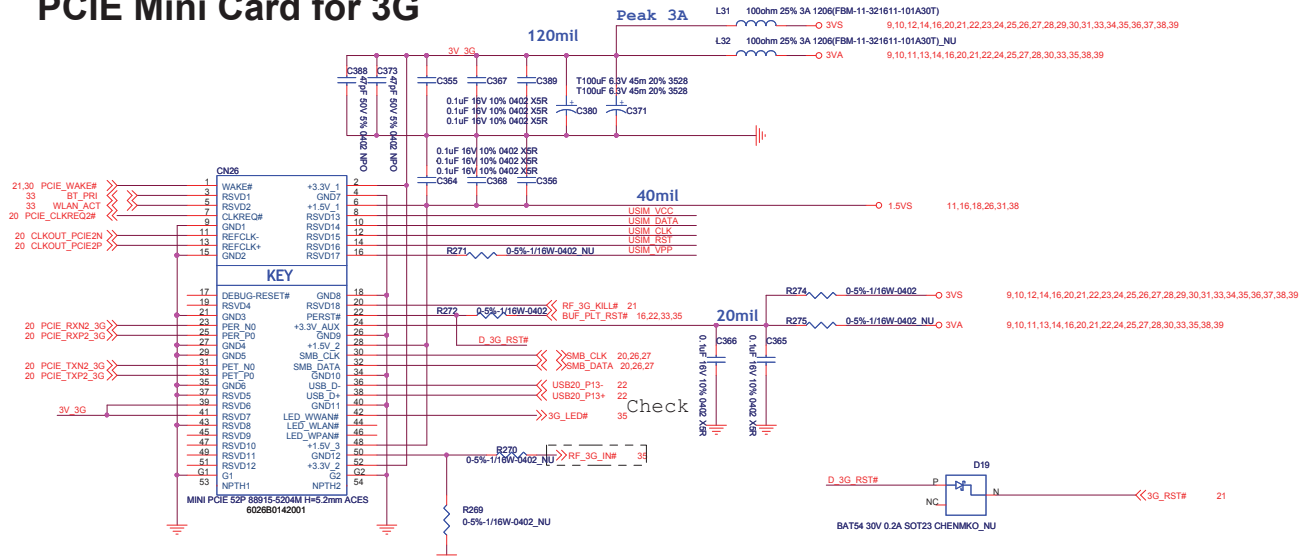


On Chip 5V to 3.3V regulator. No external regulator required.  
 On-Chip power MOSFETs for supplying flash media card power.

# SIM CARD slot



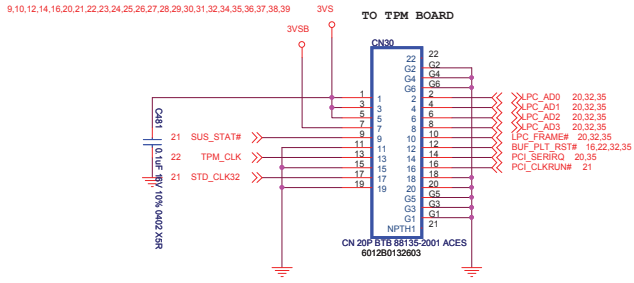
# PCIE Mini Card for 3G



<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b>			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
CHANGE BY IEC		DATE Friday, April 02, 2010	
SHEET		32 of 45	



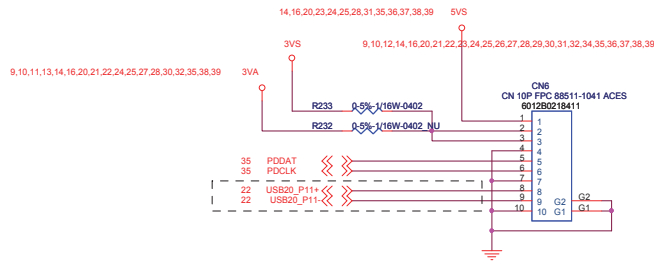
# TO TPM CNN.



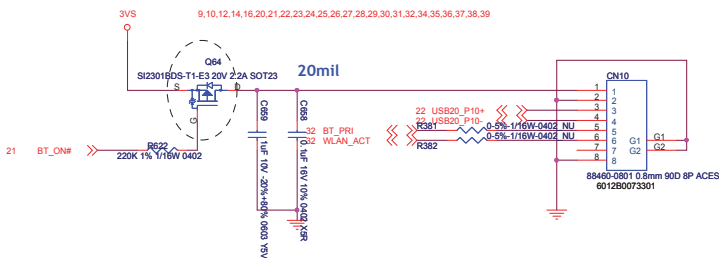
## IN TPM SKU STUFF



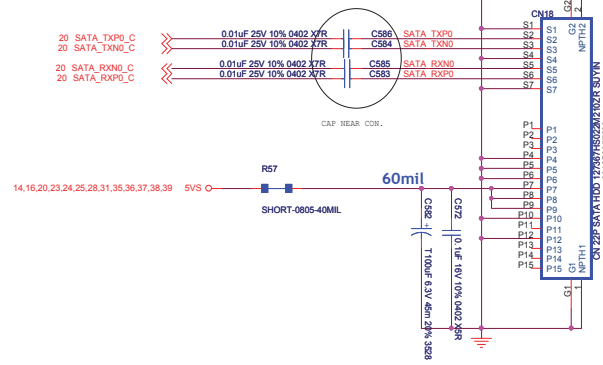
# GP + FP CNN.



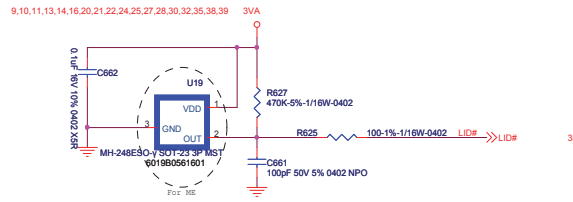
# Bluetooth CNN.



# HDD I/F



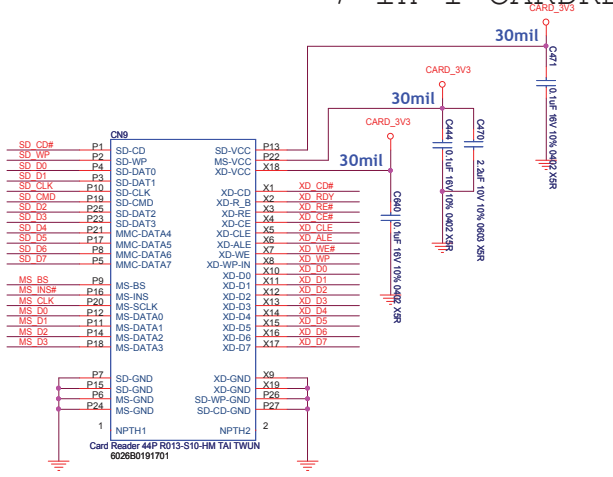
# HALL Switch



# INVENTEC

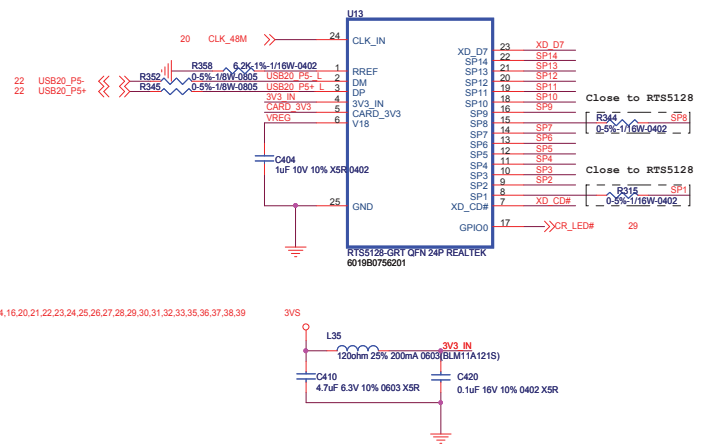
TITLE			
ACER BAP10/BXP10			
USB+SATA+G+mouse+CON			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01
SHEET		33	of 45

# 7 in 1 CARDREADER



SP1	XD_D0	SD_WP	MS_CLK
SP2	XD_RE#		MS_INS#
SP3	XD_CE#	SD_D1	MS_D7
SP4	XD_CLE	SD_D0	MS_D7
SP5	XD_ALE	SD_D7	MS_D3
SP6	XD_WEP	SD_CD#	
SP7	XD_WP	SD_D6	MS_D6
SP8	XD_D0	SD_CLK	MS_D2
SP9	XD_D1	SD_D5	MS_D0
SP10	XD_D2	SD_CMD	
SP11	XD_D3	SD_D4	MS_D4
SP12	XD_D4	SD_D3	MS_D1
SP13	XD_D5	SD_D2	MS_D5
SP14	XD_D6		MS_BS

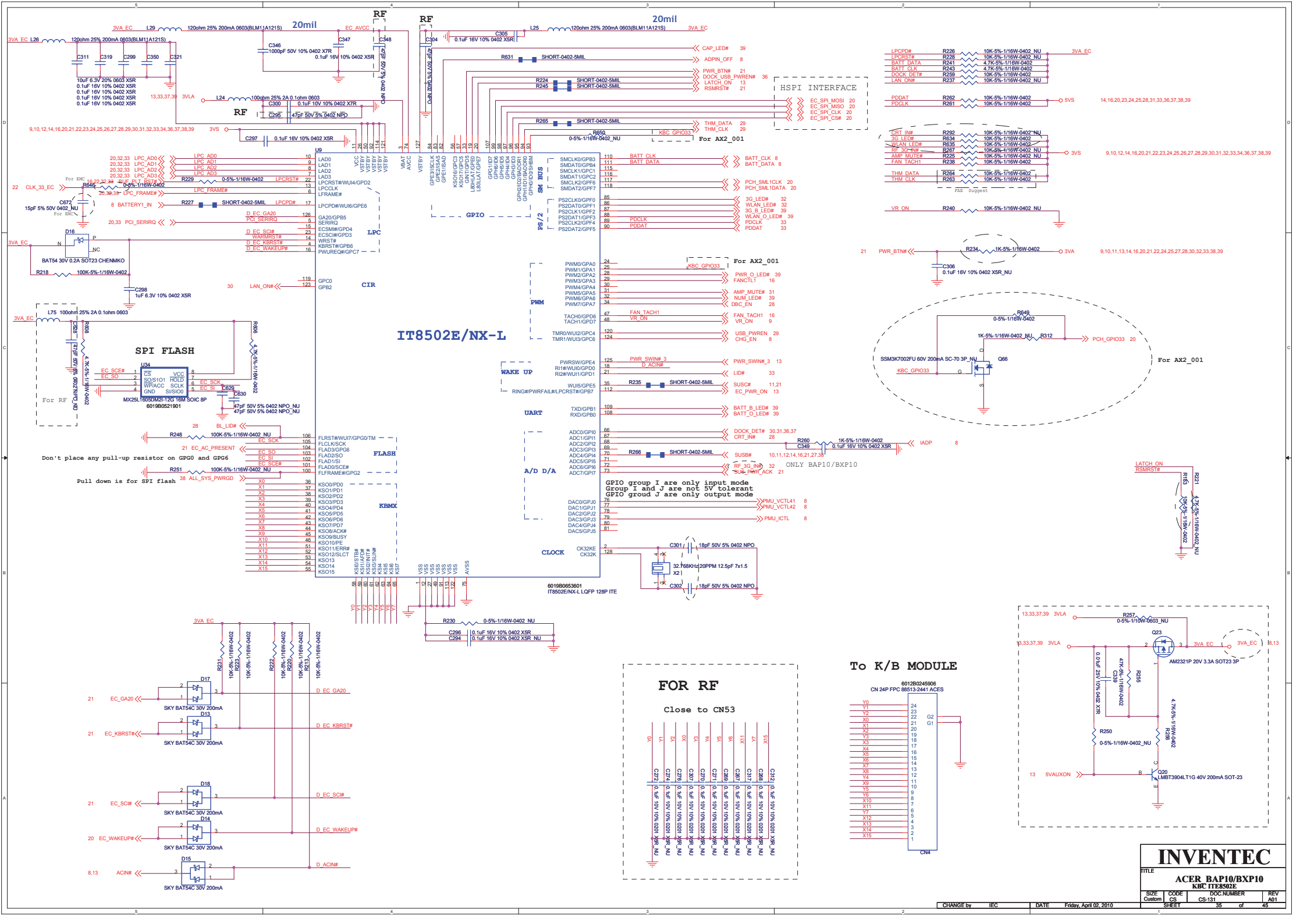
Close to connector

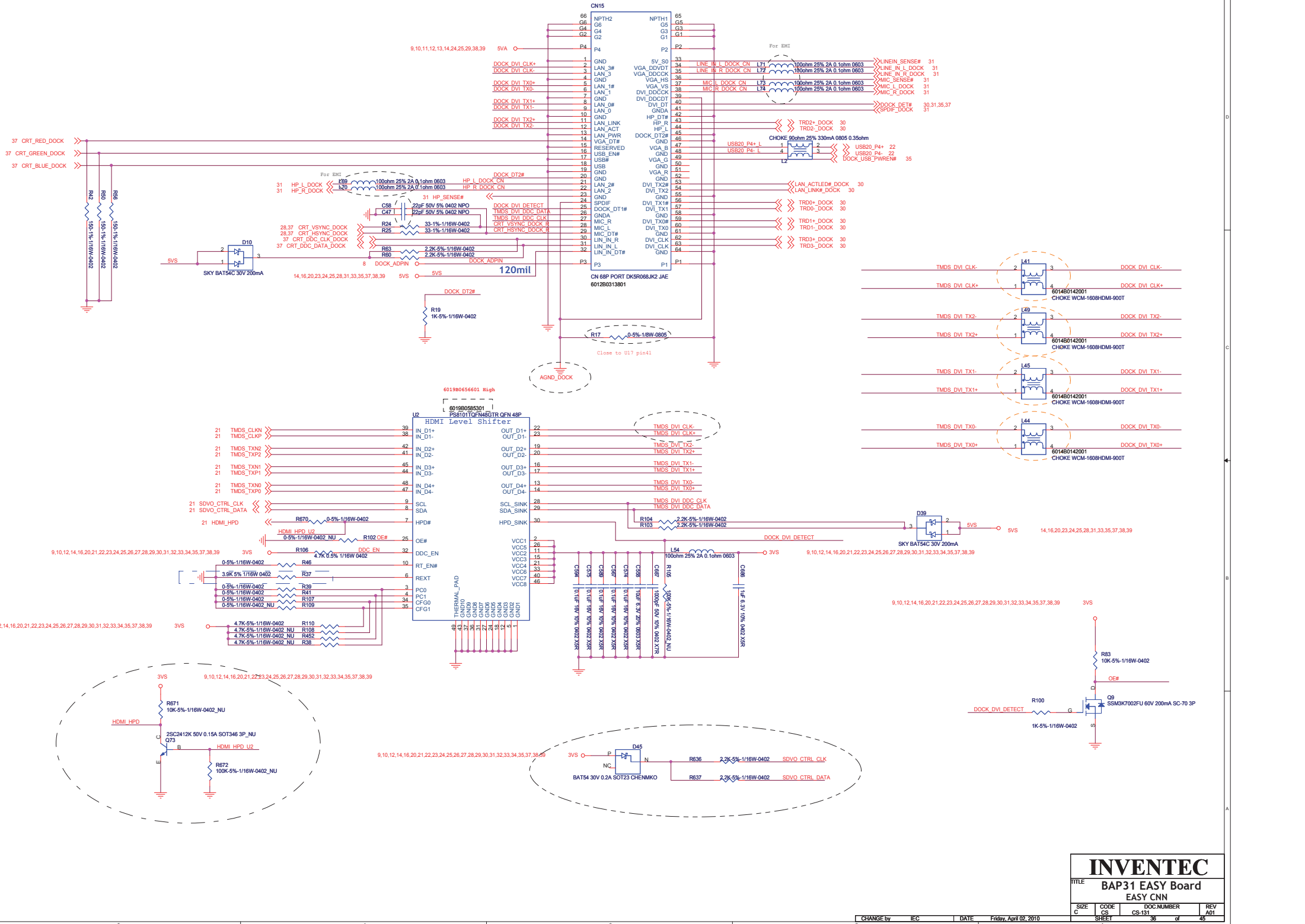


**INVENTEC**

TITLE: **BAP10 EASY Board CARD READER**

SIZE: C	CODE: A03	DOCNUMBER: 34	REV: A01
SHEET		of 45	



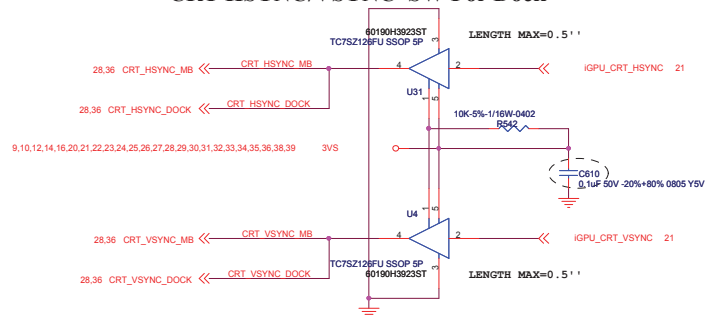


# INVENTEC

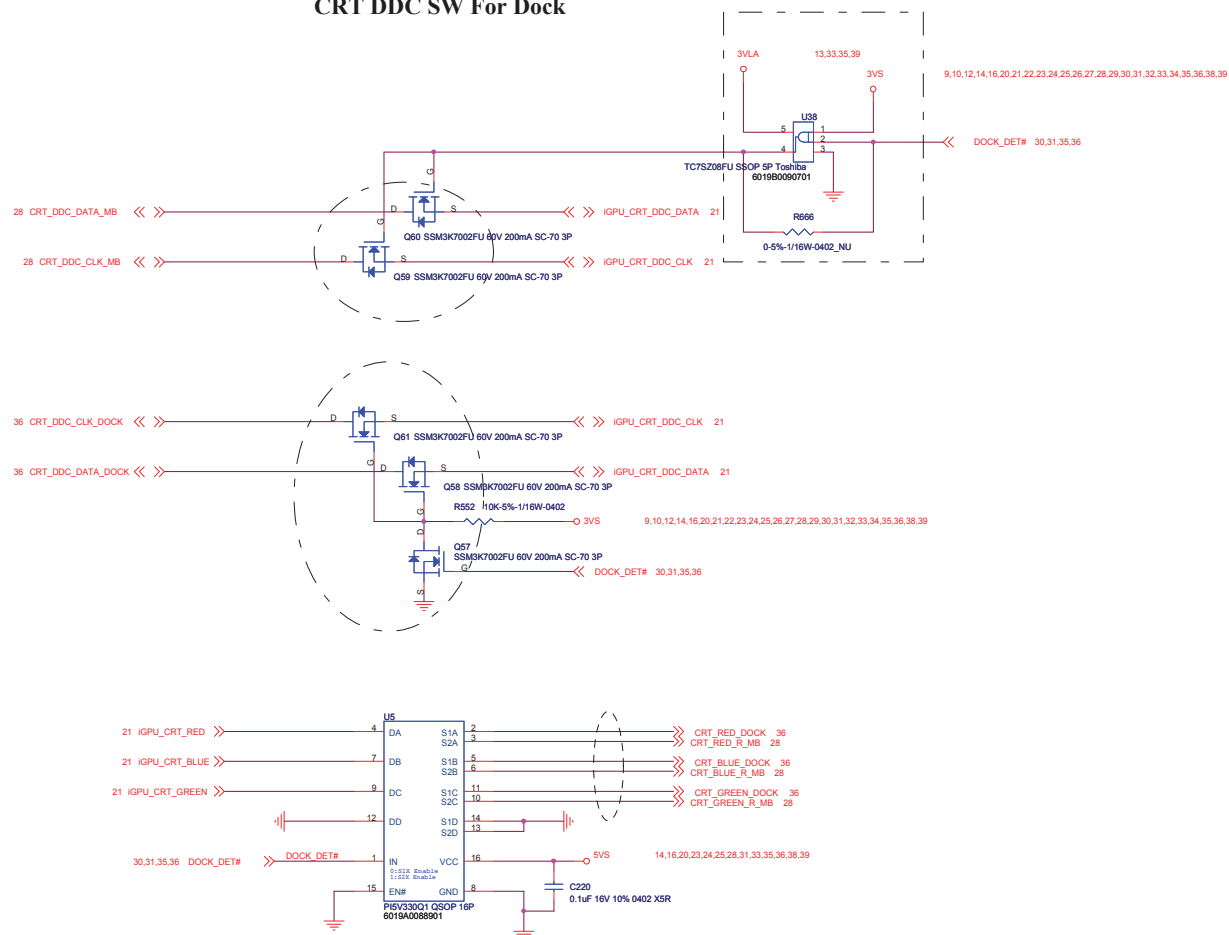
TITLE **BAP31 EASY Board**  
EASY CNN

SIZE	CODE	DWG NUMBER	REV
C	CS	CS-131	A01
SHEET	36 of 45		

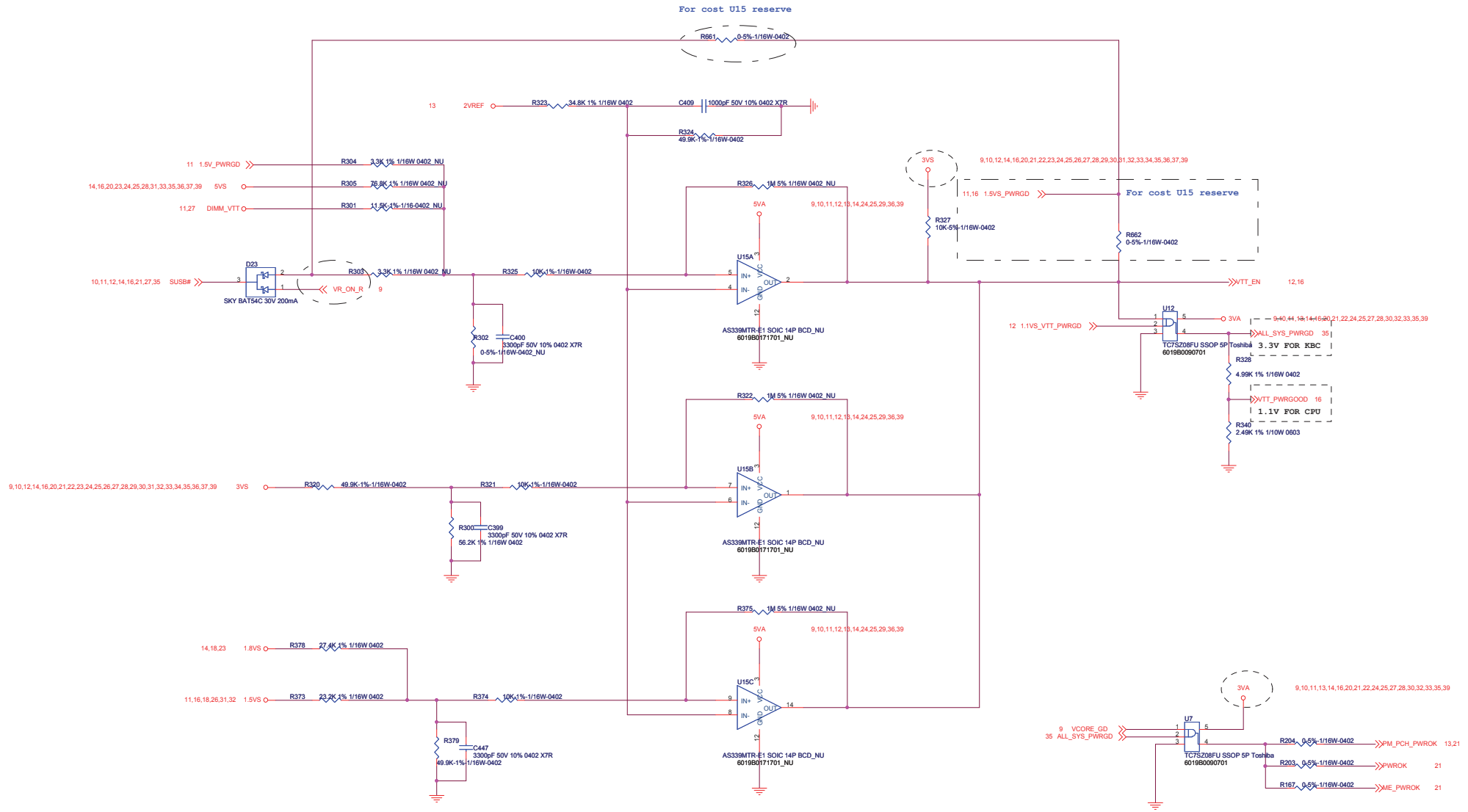
### CRT HSYNC/VSYNC SW For Dock



### CRT DDC SW For Dock



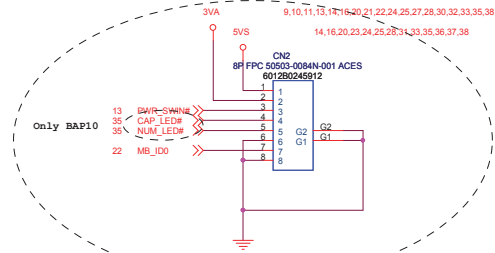
<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b>			
Hybrid Switch (1/2)			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01
CHANGE BY: IEC		DATE: Friday, April 02, 2010	SHEET 37 of 45



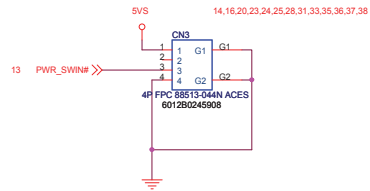
# For BAP10 SW BOARD and BXP10 LED

## Pin2 for BIOS ID setting

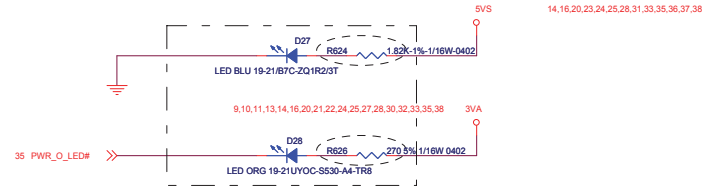
Project	MB_ID0
BAP10 (UMA)	1
BXP10 (UMA)	0



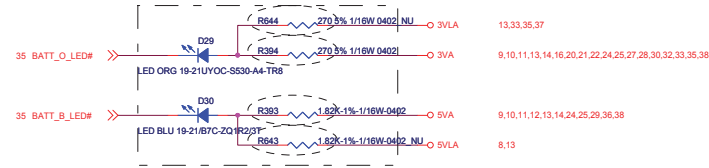
## For BXP10 SW BOARD



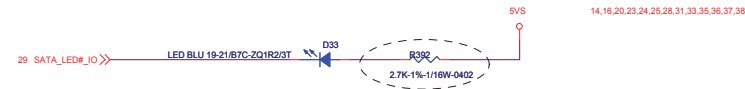
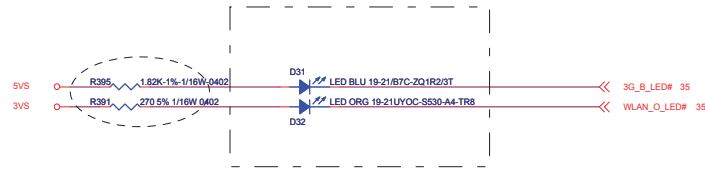
## System Power / Sleep status indicator: Blue/Orange



## Battery Charging status indicator: Blue/Orange



## Communication device status indicator: Blue/Orange



**INVENTEC**

TITLE  
**ACER BAP10/BXP10**

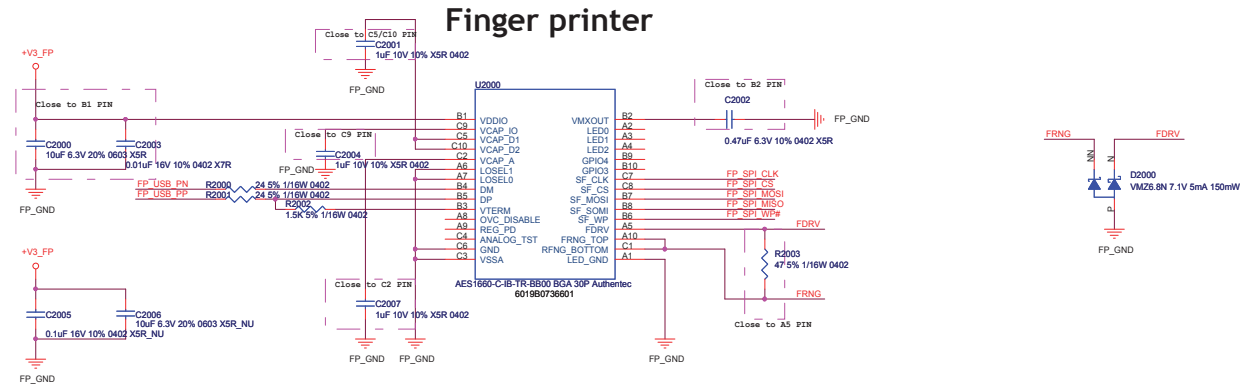
CKT/DVI

SIZE Custom CODE CS DOC NUMBER CS-131 REV A01

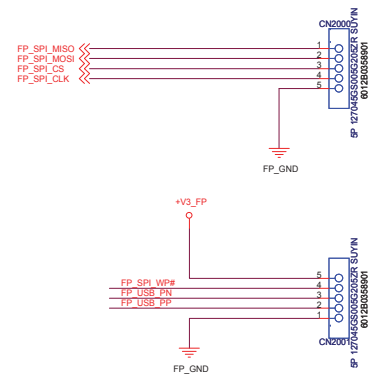
SHEET 39 of 45

# Finger printer Board

w/ Fingerprint: Stuff  
w/o Fingerprint: OPEN



## TO GP PIN HEADER



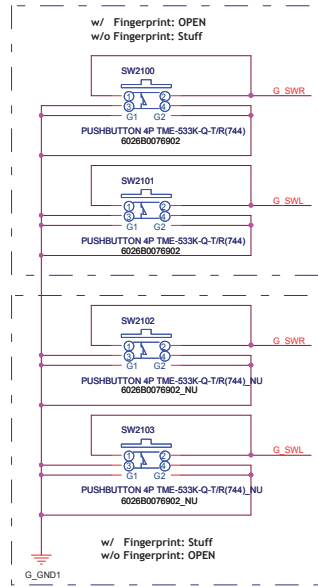
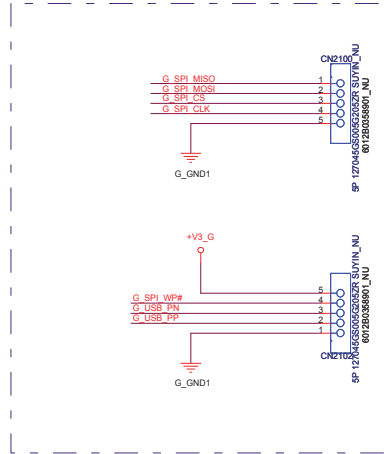
<b>INVENTEC</b>			
TITLE <b>ACER BAP10/BXP10</b>			
G/F Button			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
CHANGE by IEC		DATE Friday, April 02, 2010	SHEET 40 of 45



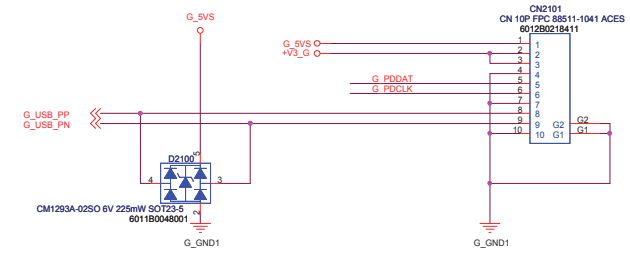
# GP Board

## GP Button

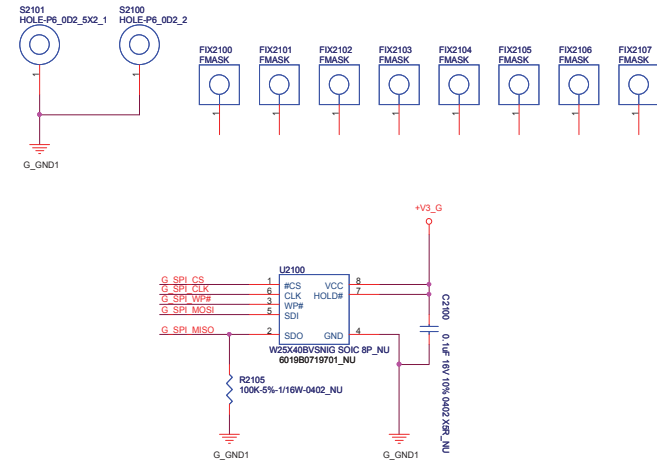
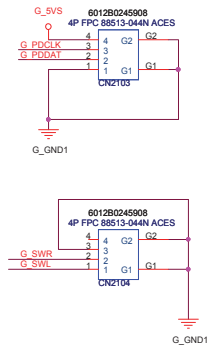
### TO FP PIN HEADER



### TO MB



### TO touch Pad

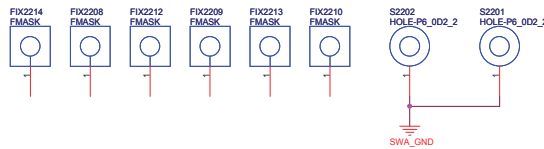
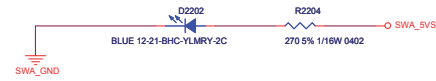
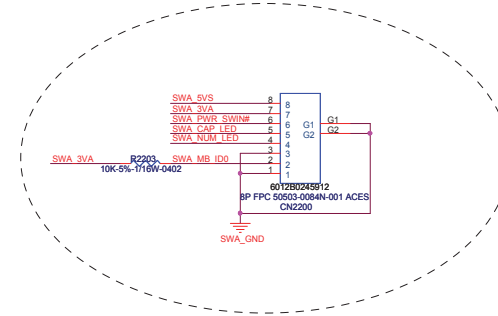
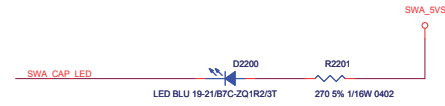
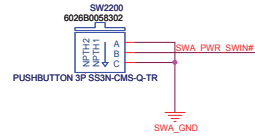


<b>INVENTEC</b>			
TITLE ACER_BAP10/BXP10			
GP			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01

# BAP10 1 SW+3 LED Board

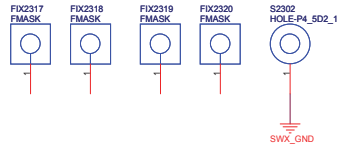
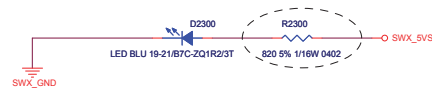
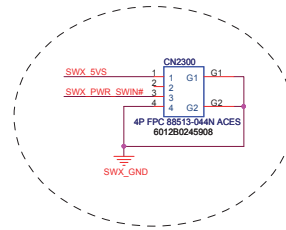
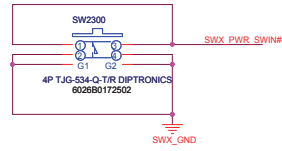
Pin2 for BIOS ID setting

Project	MB_ID0
BAP10 (UMA)	1
BXP10 (UMA)	0



<b>INVENTEC</b>			
TITLE ACER BAP10			
SIZE Custom		SW DOC NUMBER CS-131	
REV A01	REV A01	REV A01	REV A01

# BXP10 1 SW+1 LED Board

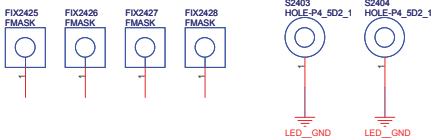
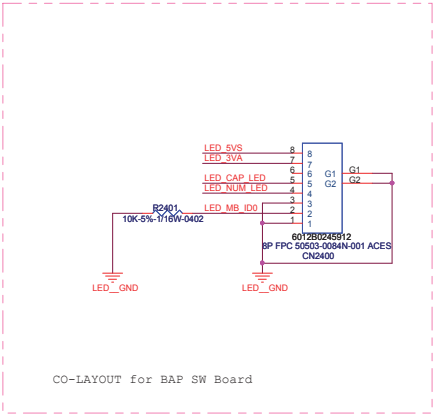
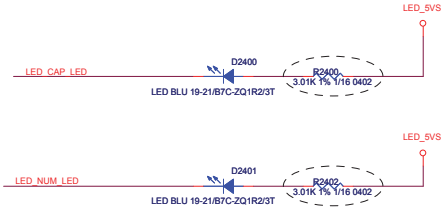


<b>INVENTEC</b>			
TITLE <b>ACER BXP10</b>			
CODE SW			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
CHANGE by IEC		DATE Friday, April 02, 2010	SHEET 43 of 45

# BXP10 2 LED Board

Pin2 for BIOS ID setting

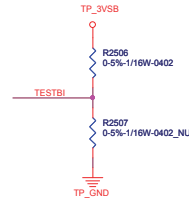
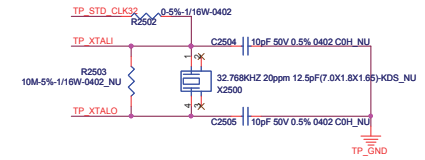
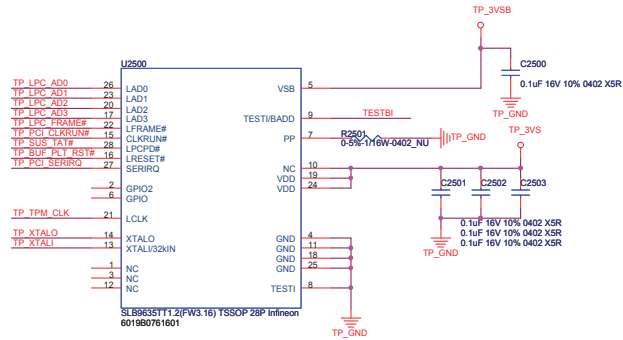
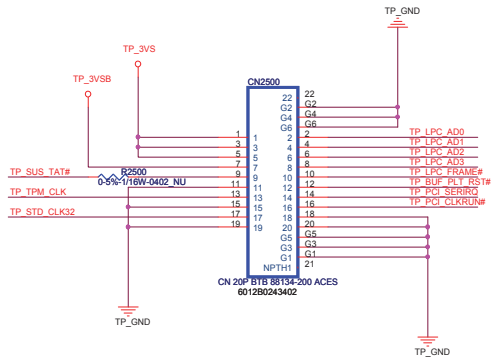
Project	MB_ID0
BAP10 (UMA)	1
BXP10 (UMA)	0



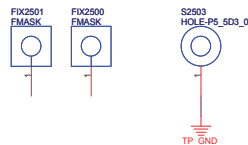
<b>INVENTEC</b>			
TITLE <b>ACER BXP10</b>			
LED			
SIZE Custom	CODE CS	DOC NUMBER CS-131	REV A01
CHANGE by IEC		DATE Friday, April 02, 2010	SHEET 44 of 45

# TPM Board

## TO MB BOARD



TESTBI	I/O ADDRESS
	CONFIG REGISTER
HIGH	4EH
LOW	2EH



<b>INVENTEC</b>			
TITLE <b>ACER_BAP10/BXP10</b>			
TPM			
SIZE	CODE	DOC NUMBER	REV
Custom	CS	CS-131	A01
PAGE		45	of 45